IRHNPC9A7110 (JANSR2N7656U9C)

PD-97969D

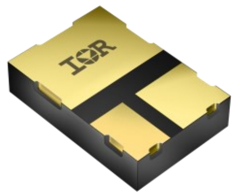
Radiation Hardened Power MOSFET
Surface Mount (SMD-0.1)
100V, 9.0A, N-channel, R9 Superjunction Technology

Features

- Single event effect (SEE) hardened (up to LET of 89.8 MeV·cm^2/mg)
- Fast switching
- Low total gate charge
- Low $R_{DS(on)}$
- Simple drive requirements
- Hermetically sealed
- Ceramic package
- Light weight
- Surface mount
- ESD rating: Class 1B per MIL-STD-750, Method 1020

Product Summary

- **BV_{DSS}:** 100V
- **I_D :** 9.0A
- **$R_{DS(on), max}$:** 150mΩ
- **$Q_{G, max}$:** 9.5nC
- **REF:** MIL-PRF-19500/790



SMD-0.1

Potential Applications

- DC-DC converter
- Motor drives
- Signal switch
- Protection switch

Product Validation

Qualified according to MIL-PRF-19500 for space applications

Description

IR HiRel R9 technology provides superior power MOSFETs for space applications. These devices have improved immunity to Single Event Effect (SEE) and have been characterized for useful performance with Linear Energy Transfer (LET) up to 89.8 MeV·cm^2/mg. Their combination of low $R_{DS(on)}$ and fast switching times will allow for better performance in applications such as DC-DC converters or motor drives. These devices retain all of the well-established advantages of MOSFETs such as voltage control, fast switching and temperature stability of electrical parameters.

Ordering Information

Table 1 Ordering options

Part number	Package	Screening level	TID level
IRHNPC9A7110	SMD-0.1	COTS	100 krad (Si)
JANSR2N7656U9C	SMD-0.1	JANS	100 krad (Si)
IRHNPC9A3110	SMD-0.1	COTS	300 krad (Si)
JANSF2N7656U9C	SMD-0.1	JANS	300 krad (Si)

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1 Absolute Maximum Ratings

Table 2 Absolute Maximum Ratings (Pre-Irradiation)

Symbol	Parameter	Value	Unit
I_{D1} @ V_{GS} = 12V, T_C = 25°C	Continuous Drain Current	9.0	A
I_{D2} @ V_{GS} = 12V, T_C = 100°C	Continuous Drain Current	6.0	A
I_{DM} @ T_C = 25°C	Pulsed Drain Current [1]	36	A
P_D @ T_C = 25°C	Maximum Power Dissipation	25	W
	Linear Derating Factor	0.2	W/°C
V_{GS}	Gate-to-Source Voltage	± 20	V
E_{AS}	Single Pulse Avalanche Energy [2]	196	mJ
I_{AR}	Avalanche Current [1]	6.0	A
E_{AR}	Repetitive Avalanche Energy [1]	2.5	mJ
dv/dt	Peak Diode Reverse Recovery [3]	21	V/ns
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C
	Lead Temperature	300 (for 5s)	°C
	Weight	0.1 (Typical)	g

[1] Repetitive Rating; Pulse width limited by maximum junction temperature.

[2] V_{DD} = 100V, starting T_J = 25°C, L = 10.9mH, Peak I_L = 6.0A, V_{GS} = 20V

[3] $I_{SD} \leq$ 9A, di/dt $\leq$ 1425A/µs, $V_{DD} \leq$ 100V, $T_J \leq$ 150°C

2 Device Characteristics

2.1 Electrical Characteristics (Pre-Irradiation)

Table 3 Static and Dynamic Electrical Characteristics @ T_j = 25°C (Unless Otherwise Specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	100	—	—	V	V_{GS} = 0V, I_D = 1.0mA
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.11	—	V/°C	Reference to 25°C, I_D = 1.0mA
$R_{DS(on)}$	Static Drain-to-Source On-State Resistance	—	—	150	mΩ	V_{GS} = 12V, I_{D2} = 6.0A [1]
$V_{GS(th)}$	Gate Threshold Voltage	2.0	—	4.0	V	$V_{DS} \geq V_{GS}$, I_D = 250μA
$\Delta V_{GS(th)}/\Delta T_J$	Gate Threshold Voltage Coefficient	—	-9.1	—	mV/°C	
Gfs	Forward Transconductance	4.0	—	—	S	V_{DS} = 15V, I_{D2} = 6.0A[1]
I_{DSS}	Zero Gate Voltage Drain Current	—	—	1.0	μA	V_{DS} = 80V, V_{GS} = 0V
		—	—	10		V_{DS} = 80V, V_{GS} = 0V, T_J = 125°C
I_{GSS}	Gate-to-Source Leakage Forward	—	—	100	nA	V_{GS} = 20V
	Gate-to-Source Leakage Reverse	—	—	-100		V_{GS} = -20V
Q_G	Total Gate Charge	—	8.2	9.5	nC	I_{D1} = 9.0A V_{DS} = 50V V_{GS} = 12V
Q_{GS}	Gate-to-Source Charge	—	3.4	4.4		
Q_{GD}	Gate-to-Drain ('Miller') Charge	—	1.3	2.3		
$t_{d(on)}$	Turn-On Delay Time	—	—	9.0	ns	I_{D1} = 9.0A ** V_{DD} = 50V R_G = 7.5Ω V_{GS} = 12V
t_r	Rise Time	—	—	12		
$t_{d(off)}$	Turn-Off Delay Time	—	—	18		
t_f	Fall Time	—	—	15		
$L_s + L_D$	Total Inductance	—	2.0	—	nH	Measured from center of Drain pad to center of Source pad
C_{iss}	Input Capacitance	—	470	—	pF	V_{GS} = 0V V_{DS} = 25V ƒ = 1.0MHz
C_{oss}	Output Capacitance	—	130	—		
C_{rss}	Reverse Transfer Capacitance	—	0.3	—		
R_G	Gate Resistance	—	5.1	—	Ω	ƒ = 1.0MHz, open drain

** Switching speed maximum limits are based on manufacturing test equipment and capability.

[1] Pulse width ≤ 300 μs; Duty Cycle ≤ 2%

2.2 Source-Drain Diode Ratings and Characteristics (Pre-Irradiation)

Table 4 Source-Drain Diode Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
I_S	Continuous Source Current (Body Diode)	—	—	9.0	A	
I_{SM}	Pulsed Source Current (Body Diode) [1]	—	—	36	A	
V_{SD}	Diode Forward Voltage	—	—	1.2	V	T_J = 25°C, I_S = 9.0A, V_{GS} = 0V [2]
t_{rr}	Reverse Recovery Time	—	87	131	ns	T_J = 25°C, I_F = 9.0A, V_{DD} ≤ 25V di/dt = 100A/µs
Q_{rr}	Reverse Recovery Charge	—	383	—	nC	
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L_S+L_D)				

2.3 Thermal Characteristics

Table 5 Thermal Resistance

Symbol	Parameter	Min.	Typ.	Max.	Unit
$R_{\theta JC}$	Junction-to-Case	—	—	5.0	°C/W
$R_{\theta J\text{-}PCB}$	Junction-to- PC Board (Soldered to 2” sq. inch copper clad board)	—	15	—	

2.4 Radiation Characteristics

IR HiRel radiation hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at IR HiRel is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 3 and 4) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

2.4.1 Electrical Characteristics – Post Total Dose Irradiation

Table 6 Electrical Characteristics @ T_j = 25°C, Post Total Dose Irradiation [3, 4]

Symbol	Parameter	Up to 300krads (Si)[5]		Unit	Test Conditions
		Min.	Max.		
BV_{DSS}	Drain-to-Source Breakdown Voltage	100	—	V	V_{GS} = 0V, I_D = 1mA
$V_{GS(th)}$	Gate Threshold Voltage	2.0	4.0	V	V_{DS} ≥ V_{GS}, I_D = 250µA
I_{GSS}	Gate-to-Source Leakage Forward	—	100	nA	V_{GS} = 20V
	Gate-to-Source Leakage Reverse	—	-100		V_{GS} = -20V
I_{DSS}	Zero Gate Voltage Drain Current	—	1.0	µA	V_{DS} = 80V, V_{GS} = 0V
$R_{DS(on)}$	Static Drain-to-Source On-State Resistance (TO-3) [2]	—	153	mΩ	V_{GS} = 12V, I_{D2} = 6.0A
$R_{DS(on)}$	Static Drain-to-Source On-State Resistance (SMD-0.1)[2]	—	150	m Ω	V_{GS} = 12V, I_{D2} = 6.0A
V_{SD}	Diode Forward Voltage	—	1.2	V	V_{GS} = 0V, I_F = 9.0A

[1] Repetitive Rating; Pulse width limited by maximum junction temperature.
[2] Pulse width ≤ 300 µs; Duty Cycle ≤ 2%
[3] Total Dose Irradiation with V_{GS} Bias. V_{GS} =-12V applied and V_{DS} = 0 during irradiation per MIL-STD-750, Method 1019, condition A.
[4] Total Dose Irradiation with V_{DS} Bias. V_{DS} = 80V applied and V_{GS} = 0 during irradiation per MIL-STD-750, Method 1019, condition A.
[5] Part numbers IRHNPC9A7110 (JANSR2N7656U9C) and IRHNPC9A3110 (JANSF2N7656U9C).

2.4.2 Single Event Effects – Safe Operating Area

IR HiRel radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. 1 and Table 7.

Table 7 Typical Single Event Effects Safe Operating Area

LET (MeV·cm²/mg)	Energy (MeV)	Range (µm)	V_{DS} (V)			
			V_{GS} = 0V	V_{GS} = -1V	V_{GS} =- 5V	V_{GS} = -10V
37 ± 5%	417 ± 7.5%	50 ± 7.5%	100	100	100	100
59.8 ± 5%	753 ± 7.5%	60 ± 7.5%	100	100	100	100
89.8 ± 5%	1515 ± 7.5%	82 ± 7.5%	100	100	—	—

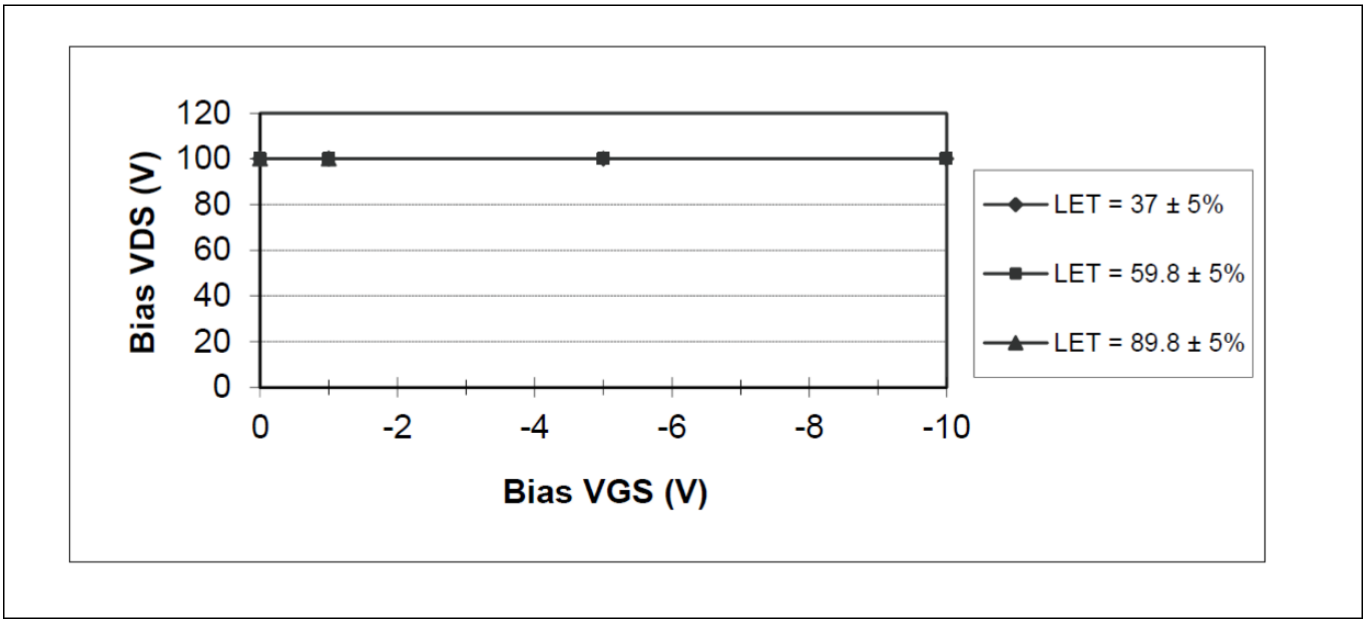


Figure 1 Typical Single Event Effect, Safe Operating Area

3 Electrical Characteristics Curves (Pre-irradiation)

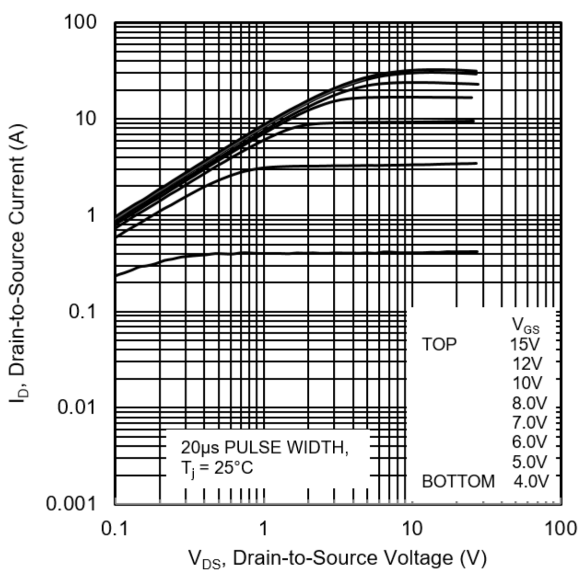


Figure 2 **Typical Output Characteristics**

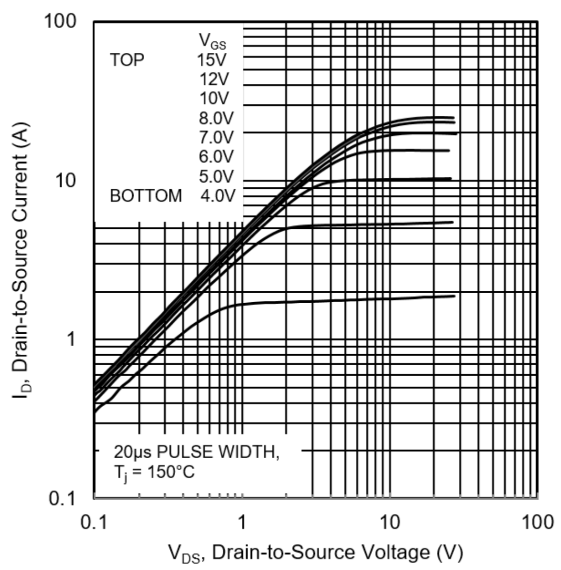


Figure 3 **Typical Output Characteristics**

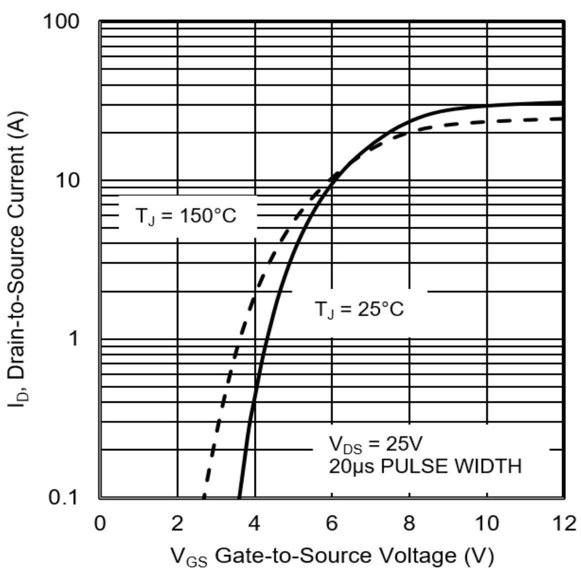


Figure 4 **Typical Transfer Characteristics**

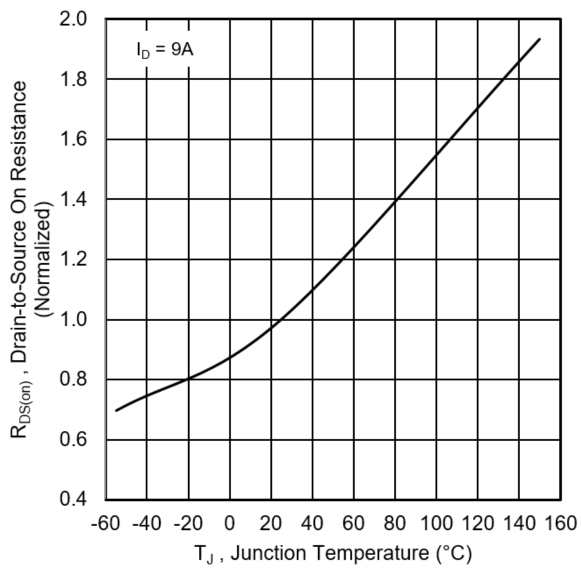


Figure 5 **Normalized On-Resistance Vs. Temperature**

Electrical Characteristics Curves (Pre-irradiation)

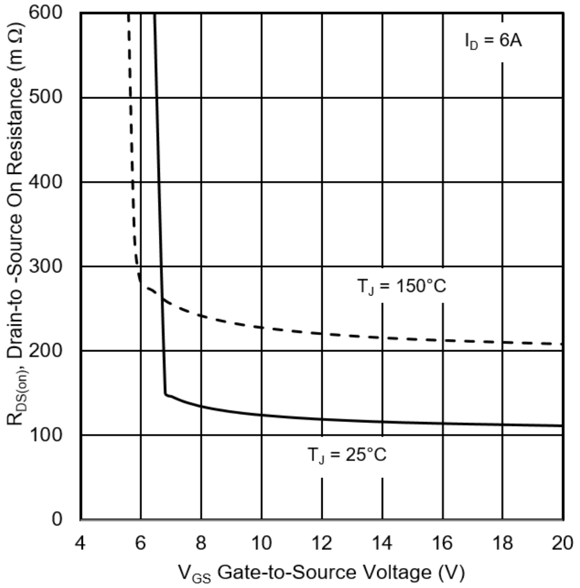


Figure 6 **Typical On-Resistance Vs. Gate Voltage**

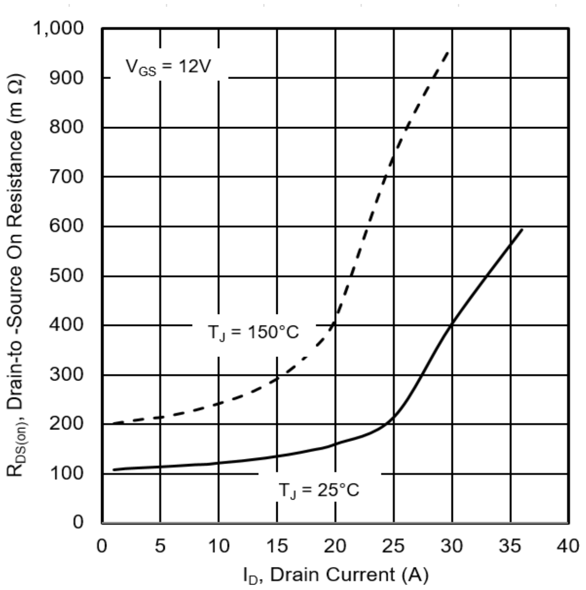


Figure 7 **Typical On-Resistance Vs. Drain Current**

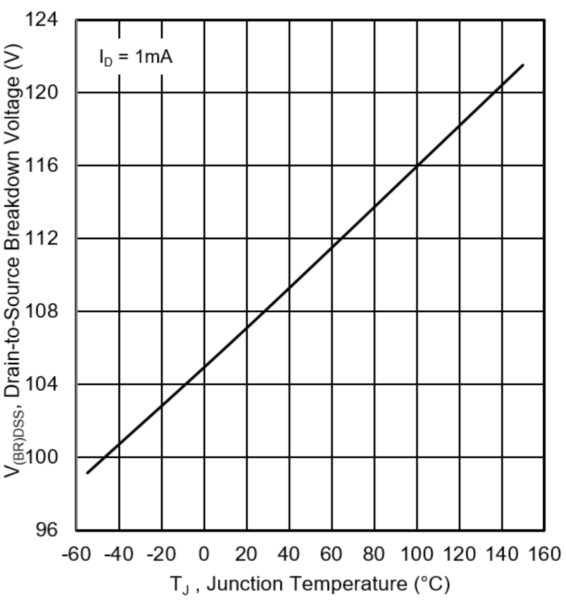


Figure 8 **Typical Drain-to-Source Breakdown Voltage Vs. Temperature**

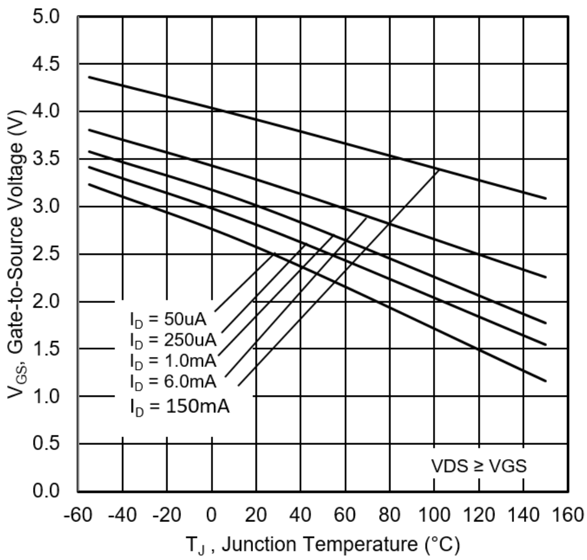


Figure 9 **Typical Threshold Voltage Vs. Temperature**

Electrical Characteristics Curves (Pre-irradiation)

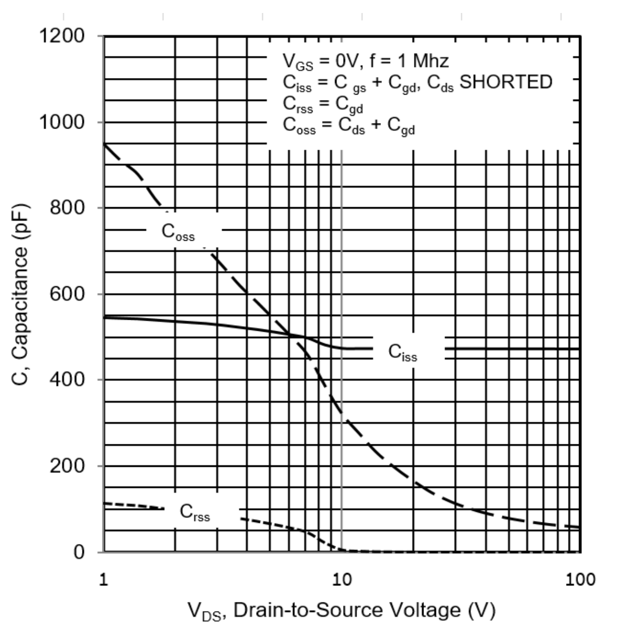


Figure 10 Typical Capacitance Vs. Drain-to-Source Voltage

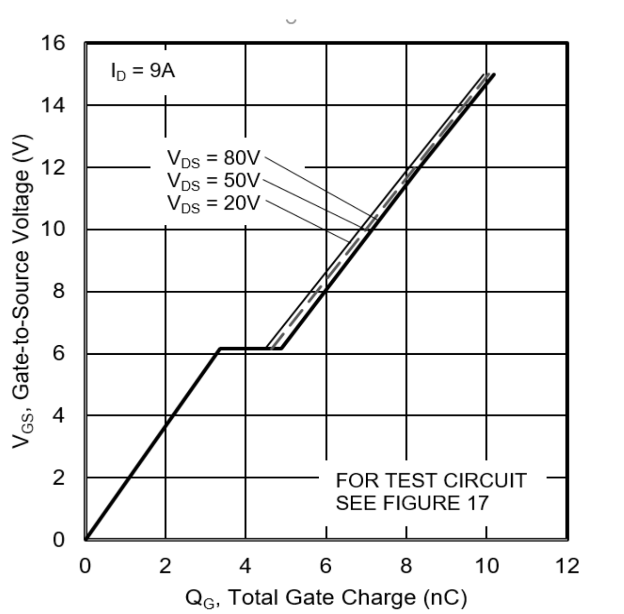


Figure 11 Typical Gate Charge Vs. Gate-to-Source Voltage

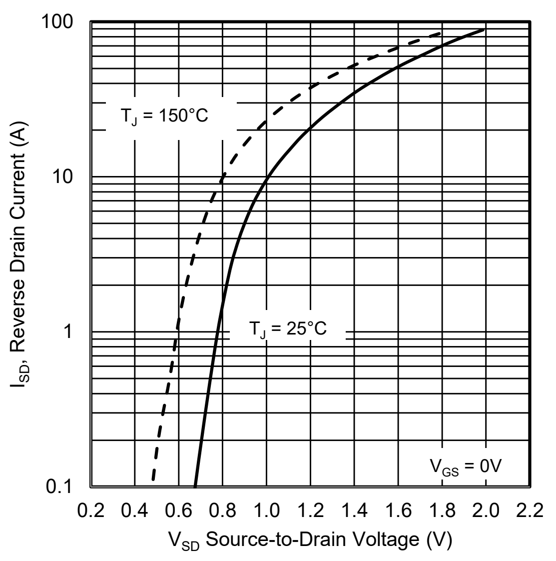


Figure 12 Typical Source-Drain Vs. Diode Forward Voltage

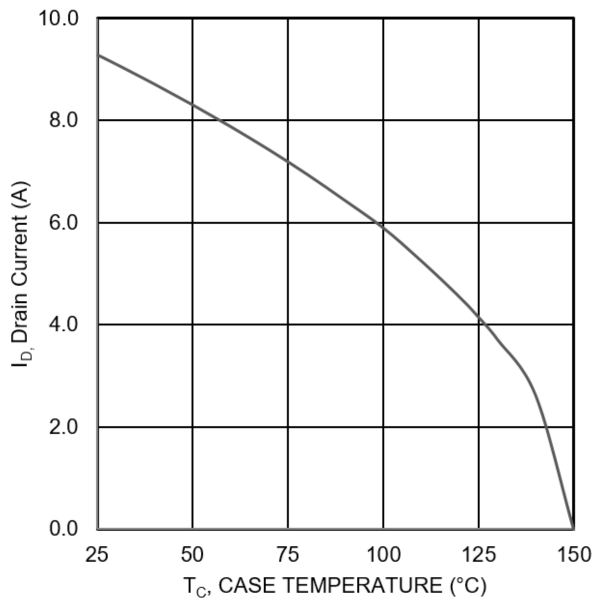


Figure 13 Maximum Drain Current Vs. Case Temperature

Electrical Characteristics Curves (Pre-irradiation)

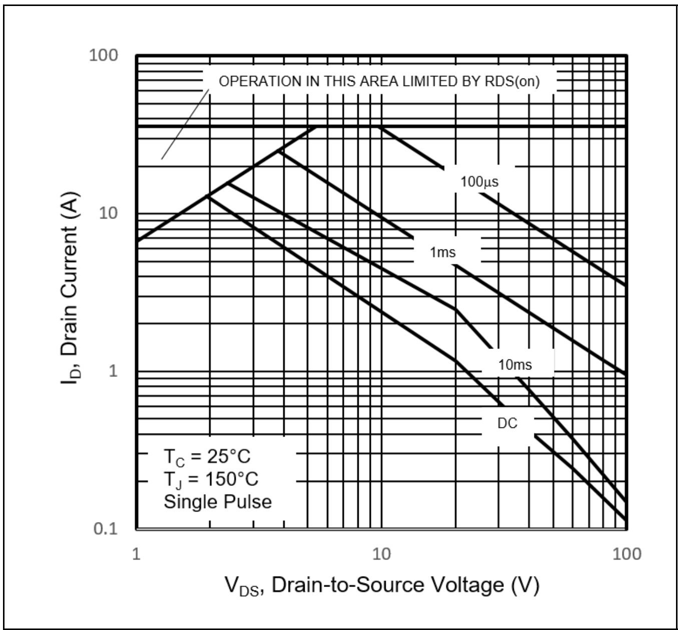


Figure 14 **Maximum Safe Operating Area**

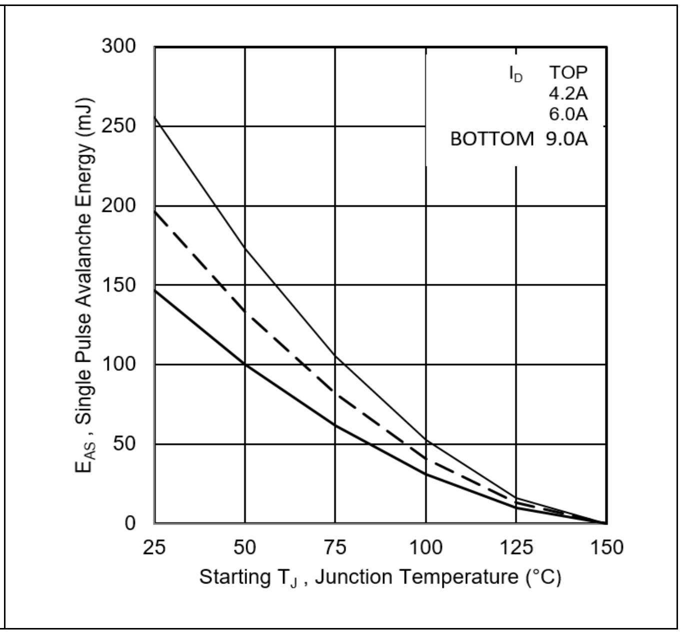


Figure 15 **Maximum Avalanche Energy Vs. Junction Temperature**

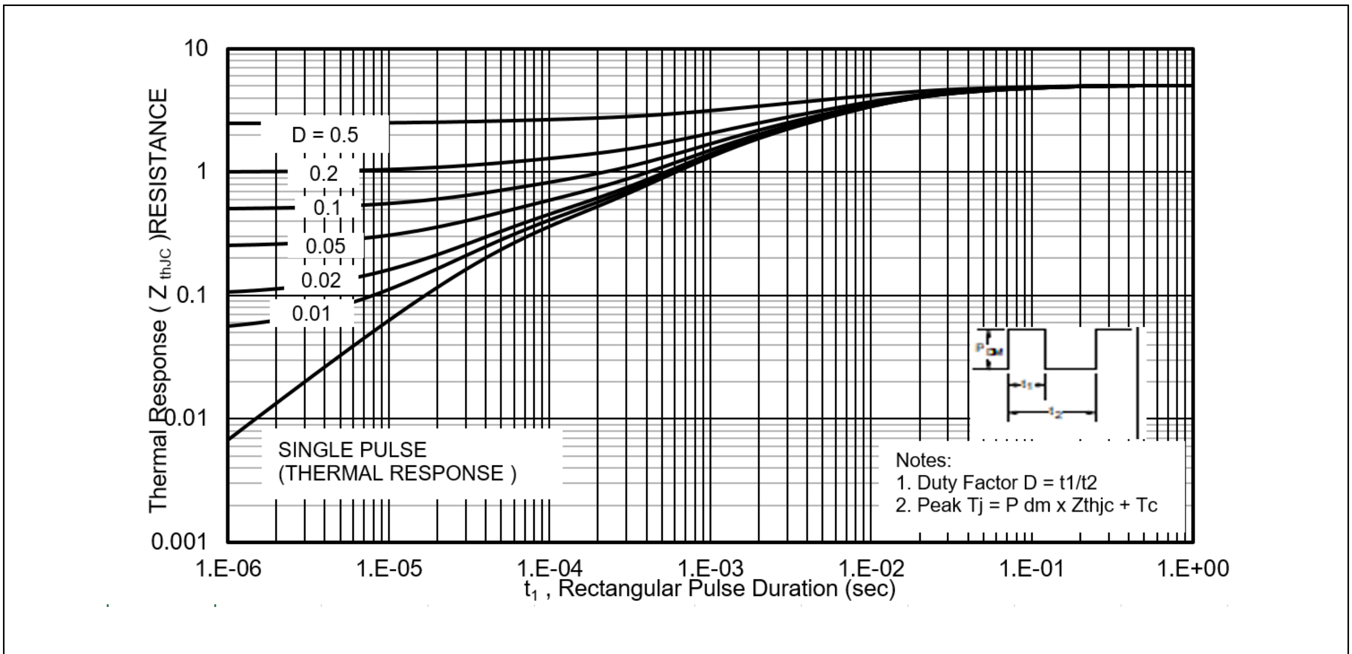


Figure 16 **Maximum Effective Transient Thermal Impedance, Junction-to-Case**

4 Test Circuits (Pre-irradiation)

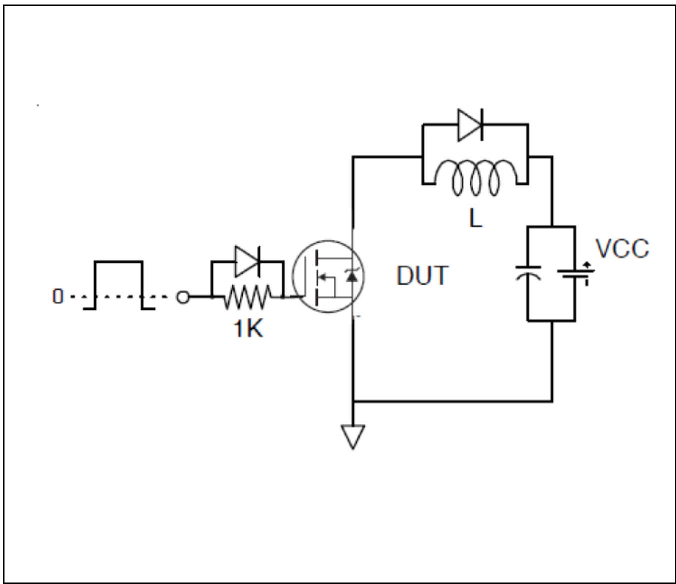


Figure 17 Gate Charge Test Circuit

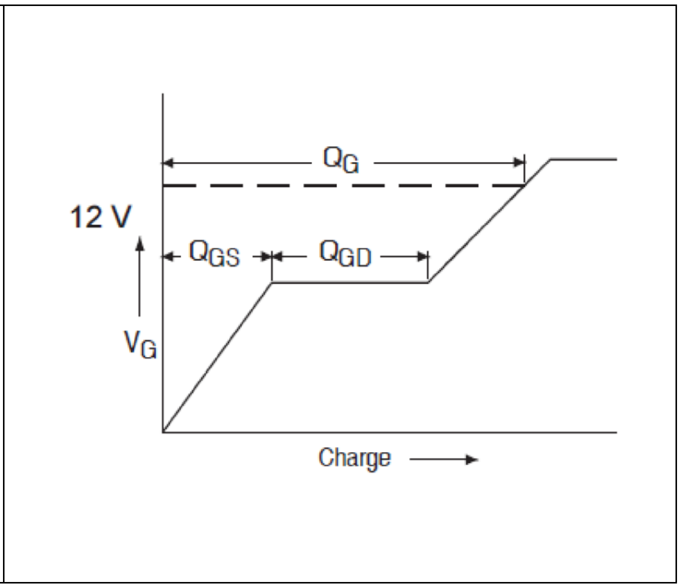


Figure 18 Gate Charge Waveform

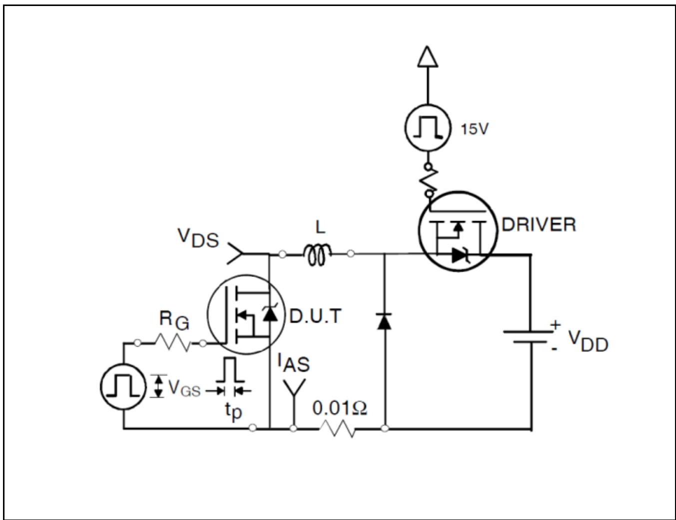


Figure 19 Unclamped Inductive Test Circuit

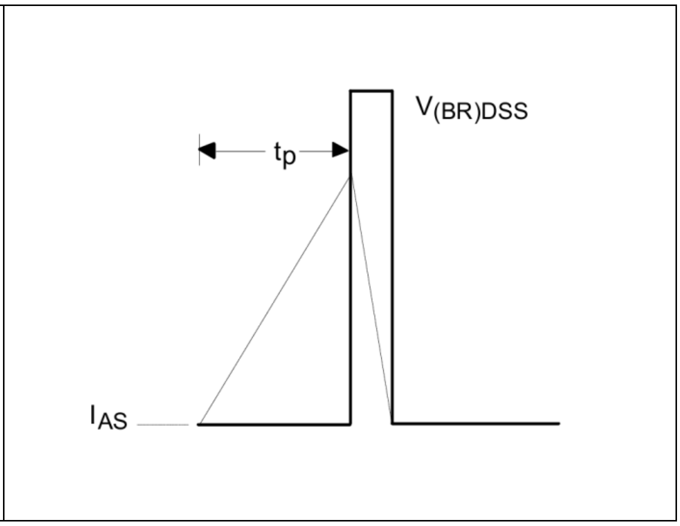


Figure 20 Unclamped Inductive Waveform

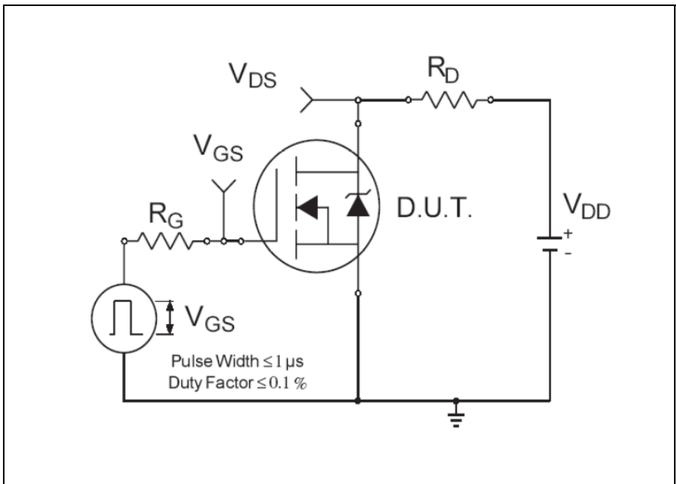


Figure 21 Switching Time Test Circuit

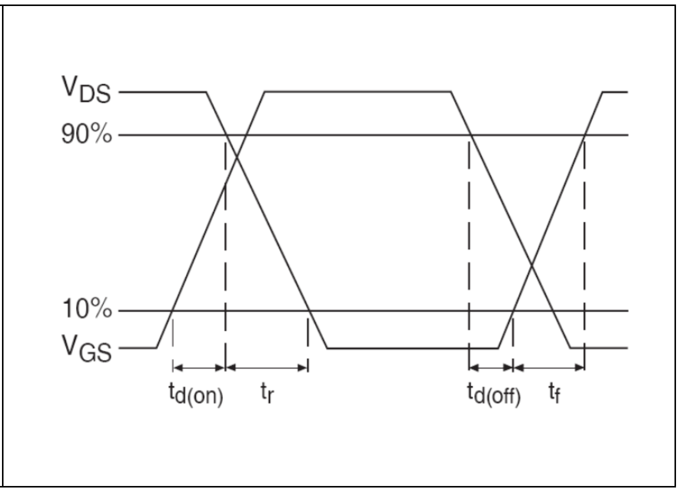
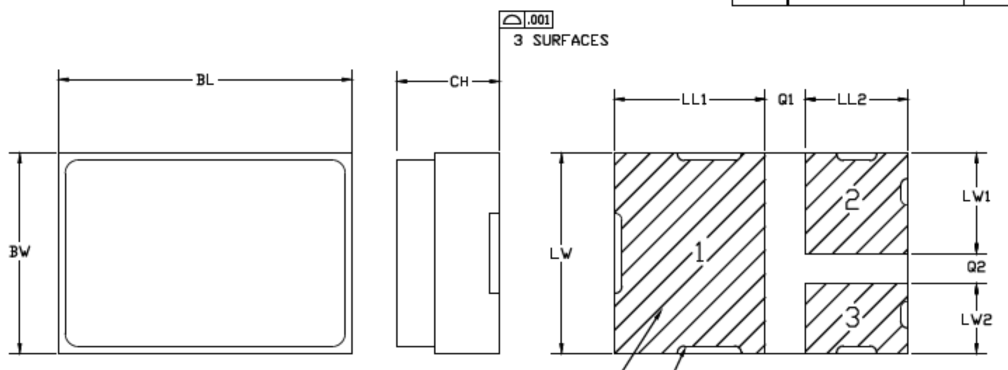


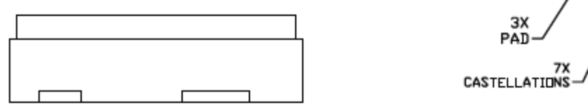
Figure 22 Switching Time Waveforms

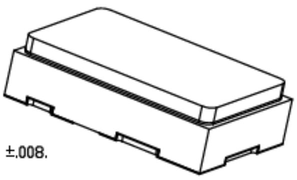
5 Package Outline

Note: For the most updated package outline, please see the website: SMD-0.1

REV.	DESCRIPTION	ECN	DATE
E	CHANGE PER ECN	1120_09447	2-2-23







Symbol	Dimensions			
	Inches		Millimeters	
	Min	Max	Min	Max
BL	0.215	0.225	5.46	5.72
BW	0.145	0.155	3.68	3.94
CH		0.077		1.96
LW1	0.070	0.080	1.78	2.03
LW2	0.047	0.057	1.19	1.45
LW	0.142	0.158	3.61	4.01
LL1	0.108	0.118	2.74	3.00
LL2	0.072	0.082	1.83	2.08
Q1	.030		0.76	
Q2	.022		0.56	

NOTES:
1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].
4. UNLESS OTHERWISE SPECIFIED DIMENSION TOLERANCE IS ±.008.
5. PADS PULLBACK FROM EDGE OF PART 0.010 MAX.
6. STANDARD FINAL LEAD FINISH IS SOLDER ALLOY 63%Sn 37%Pb.

IOR HiRel
An Infineon Technologies Company

PAD ASSIGNMNT
1 = DRAIN
2 = SOURCE
3 = GATE

TITLE:
CASE OUTLINE, SMD 0.1 C350075

DRAWING NO.
D101018G-WEB

REV E

Revision history

Document version	Date of release	Description of changes
	10/16/2023	Preliminary datasheet with PPD number (PPD-97969B)
Rev C	11/29/2023	Final datasheet with PD number
Rev D	11/15/2024	Updated based on ECN- 1120_10121

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