

Radiation Hardened Power MOSFET Thru-Hole (TO-257AA) 30V, 18A, N-channel, R5 Technology

#### Features

- Single event effect (SEE) hardened
- Low R<sub>DS(on)</sub>
- Low total gate charge
- Simple drive requirements
- Hermetically sealed
- Electrically isolated
- Ceramic eyelets
- ESD rating: Class 1C per MIL-STD-750, Method 1020

# **Potential Applications**

- DC-DC converter
- Motor drives

## **Product Validation**

Qualified to JANS screening flow according to MIL-PRF-19500 for space applications

### Description

IR HiRel rad hard HEXFET technology provides high performance power MOSFETs for space applications. This technology has over a decade of proven performance and reliability in satellite applications. These devices have been characterized for both Total Dose and Single Event Effects (SEE). The combination of low  $R_{DS(on)}$  and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well-established advantages of MOSFETs such as voltage control, fast switching and temperature stability of electrical parameters.

#### **Ordering Information**

Table 1 Ordering options							
Part number	Package	Screening Level	TID Level				
IRHY57Z30CM	TO-257AA	СОТЅ	100 krad(Si)				
JANSR2N7482T3	TO-257AA	JANS	100 krad(Si)				
IRHY53Z30CM	TO-257AA	COTS	300 krad(Si)				
JANSF2N7482T3	TO-257AA	JANS	300 krad(Si)				
IRHY54Z30CM	TO-257AA	СОТЅ	500 krad(Si)				
JANSG2N7482T3	TO-257AA	JANS	500 krad(Si)				

# **Product Summary**

- **BV**<sub>DSS</sub>: 30V
- I<sub>D</sub>:18A
- **R**<sub>DS(on),max</sub>: 30mΩ
- **Q**<sub>G,max</sub>: 65nC
- **REF:** MIL-PRF-19500/702



PD-93824G



#### Table of contents

# **Table of contents**

Featu	Ires	1
Pote	ntial Applications	1
Prod	uct Validation	1
Desc	ription	1
Orde	ring Information	1
Table	e of contents	2
1	Absolute Maximum Ratings	3
2	Device Characteristics	4
2.1	Electrical Characteristics (Pre-Irradiation)	,4
2.2	Source-Drain Diode Ratings and Characteristics (Pre-Irradiation)	5
2.3	Thermal Characteristics	5
2.4	Radiation Characteristics	5
2.4.1	Electrical Characteristics — Post Total Dose Irradiation	5
2.4.2	Single Event Effects — Safe Operating Area	6
3	Electrical Characteristics Curves (Pre-irradiation)	7
4	Test Circuits (Pre-irradiation)1	.0
5	Package Outline1	.1
Revis	ion history1	.2

# Radiation Hardened Power MOSFET Thru-Hole (TO-257AA)



**Absolute Maximum Ratings** 

# **1** Absolute Maximum Ratings

#### Table 2 Absolute Maximum Ratings (Pre-Irradiation)

Symbol	Parameter	Value	Unit
$I_{D1} @ V_{GS} = 12V, T_C = 25^{\circ}C$	Continuous Drain Current	18*	Α
$I_{D2} @ V_{GS} = 12V, T_{C} = 100^{\circ}C$	Continuous Drain Current	18*	А
I <sub>DM</sub> @ T <sub>C</sub> = 25°С	Pulsed Drain Current <sup>1</sup>	72	Α
$P_{D} @ T_{C} = 25^{\circ}C$	Maximum Power Dissipation	75	W
	Linear Derating Factor	0.6	W/°C
V <sub>GS</sub>	Gate-to-Source Voltage	± 20	V
E <sub>AS</sub> Single Pulse Avalanche Energy <sup>2</sup>		177	mJ
I <sub>AR</sub> Avalanche Current <sup>1</sup>		18	А
E <sub>AR</sub>	Repetitive Avalanche Energy <sup>1</sup>	7.5	mJ
dv/dt	Peak Diode Reverse Recovery <sup>3</sup>	1.7	V/ns
T」 T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to +150	°C
	Lead Temperature	300 (0.063 in. /1.6 mm from case for 10s)	
	Weight	4.3 (Typical)	g

\*Current is limited by package

 $<sup>^{\</sup>rm 1}$  Repetitive Rating; Pulse width limited by maximum junction temperature.

 $<sup>^2</sup>$  V\_{DD} = 15V, starting T\_J = 25°C, L = 1.0mH, Peak I\_L = 18A, V\_{GS} = 12V

 $<sup>^3</sup>$  I\_{SD}  $\leq$  18A,  $di/dt \leq$  54A/µs,  $V_{DD} \leq$  30V,  $T_{\rm J} \leq$  150°C

#### Radiation Hardened Power MOSFET Thru-Hole (TO-257AA)



**Device Characteristics** 

# 2 Device Characteristics

## 2.1 Electrical Characteristics (Pre-Irradiation)

#### Table 3 Static and Dynamic Electrical Characteristics @ T<sub>j</sub> = 25°C (Unless Otherwise Specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions		
BV <sub>DSS</sub>	Drain-to-Source Breakdown Voltage	30	_	_	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1.0mA		
$\Delta {\sf BV}_{\sf DSS}/\Delta {\sf T}_{\sf J}$	Breakdown Voltage Temp. Coefficient	_	0.028	_	V/°C	Reference to 25°C, $I_D = 1.0 m/$		
R <sub>DS(on)</sub>	Static Drain-to-Source On-State Resistance	_	_	30	mΩ	$V_{GS} = 12V$ , $I_{D2} = 18A^{-1}$		
$V_{GS(th)}$	Gate Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}, I_{D} = 1mA$		
Gfs	Forward Transconductance	16	_	_	S	$V_{DS} = 15V, I_{D2} = 18A^{1}$		
1	Zara Cata Valtaga Drain Current	—	_	10		$V_{DS} = 24V, V_{GS} = 0V$		
DSS	Zero Gate Voltage Drain Current	—		25	μA	$V_{DS} = 24V, V_{GS} = 0V, T_J = 125^{\circ}C$		
1	Gate-to-Source Leakage Forward	—		100		V <sub>GS</sub> = 20V		
I <sub>GSS</sub>	Gate-to-Source Leakage Reverse	—		-100	nA	V <sub>GS</sub> = -20V		
Q <sub>G</sub>	Total Gate Charge	—	_	65		I <sub>D1</sub> = 18A		
Q <sub>GS</sub>	Gate-to-Source Charge	—	_	20	nC	$V_{DS} = 15V$ $V_{GS} = 12V$		
Q <sub>GD</sub>	Gate-to-Drain ('Miller') Charge	—		10				
t <sub>d(on)</sub>	Turn-On Delay Time	—		25		I <sub>D1</sub> = 18A **		
t <sub>r</sub>	Rise Time	_	_	100		$V_{DD} = 15V$		
t <sub>d(off)</sub>	Turn-Off Delay Time	_	_	35	ns	$R_{G} = 7.5\Omega$		
t <sub>f</sub>	Fall Time	_	_	30		$V_{GS} = 12V$		
L <sub>s</sub> +L <sub>D</sub>	Total Inductance	_	6.8	_	nH	Measured from Drain lead (6mm / 0.25 in from packag to Source lead (6mm/ 0.25 in from package) with Sourc wire internally bonded from Source pin to Drain pad		
C <sub>iss</sub>	Input Capacitance	_	2054	—		V <sub>GS</sub> = 0V		
C <sub>oss</sub>	Output Capacitance		936	—	pF	$V_{DS} = 25V$		
C <sub>rss</sub>	Reverse Transfer Capacitance	—	33	—		<i>f</i> = 1.0MHz		

\*\* Switching speed maximum limits are based on manufacturing test equipment and capability.

 $<sup>^1</sup>$  Pulse width  $\leq$  300  $\mu s$ ; Duty Cycle  $\leq$  2%

### IRHY57Z30CM (JANSR2N7482T3) Radiation Hardened Power MOSFET Thru-Hole (TO-257AA)



**Device Characteristics** 

# 2.2 Source-Drain Diode Ratings and Characteristics (Pre-Irradiation)

#### Table 4 Source-Drain Diode Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions	
ls	Continuous Source Current (Body Diode)	-	_	18	А		
I <sub>SM</sub>	Pulsed Source Current (Body Diode) <sup>1</sup>	_	_	72	А		
V <sub>SD</sub>	Diode Forward Voltage	-	_	1.2	V	$T_J$ = 25°C, $I_S$ = 18A, $V_{GS}$ = 0V <sup>-2</sup>	
t <sub>rr</sub>	Reverse Recovery Time	-	_	102	ns	$T_J = 25^{\circ}C, I_F = 18A, V_{DD} \le 25V$ di/dt = 100A/µs <sup>2</sup>	
Q <sub>rr</sub>	Reverse Recovery Charge	-	_	193	nC		
t <sub>on</sub>	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by $L_s+L_D$ )					

# 2.3 Thermal Characteristics

#### Table 5 Thermal Resistance

Symbol	Parameter	Min.	Тур.	Max.	Unit
$R_{\theta JC}$	Junction-to-Case	_	_	1.67	°CIM
$R_{\theta JA}$	Junction-to- Ambient (Typical socket mount)	_	_	80	°C/W

# 2.4 Radiation Characteristics

IR HiRel radiation hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at IR HiRel is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 3 and 4) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

# 2.4.1 Electrical Characteristics – Post Total Dose Irradiation

#### Table 6Electrical Characteristics @ T<sub>J</sub> = 25°C, Post Total Dose Irradiation <sup>3, 4</sup>

Symbol	Demonster	Up to 50	00 krad (Si)⁵			
	Parameter	Min.	Max.	Unit	Test Conditions	
BV <sub>DSS</sub>	Drain-to-Source Breakdown Voltage	30	_	V	$V_{GS} = 0V, I_{D} = 1.0 mA$	
$V_{GS(th)}$	Gate Threshold Voltage	2.0	4.0	V	$V_{DS} = V_{GS}, I_D = 1.0 \text{mA}$	
I <sub>GSS</sub>	Gate-to-Source Leakage Forward	_	100		V <sub>GS</sub> = 20V	
	Gate-to-Source Leakage Reverse	rce Leakage Reverse — -100 n/		nA	$V_{GS} = -20V$	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	_	10	μΑ	$V_{DS} = 24V, V_{GS} = 0V$	
R <sub>DS(on)</sub>	Static Drain-to-Source On-State Resistance (TO-3) <sup>2</sup>	_	25	mΩ	$V_{GS} = 12V, I_{D2} = 18A$	
R <sub>DS(on)</sub>	Static Drain-to-Source On-State Resistance (TO-257AA) <sup>2</sup>	_	30	mΩ	$V_{GS} = 12V, I_{D2} = 18A$	
V <sub>SD</sub>	Diode Forward Voltage	_	1.2	V	$V_{GS} = 0V, I_F = 18A$	

<sup>&</sup>lt;sup>1</sup> Repetitive Rating; Pulse width limited by maximum junction temperature.

 $<sup>^2</sup>$  Pulse width  $\leq$  300  $\mu s$ ; Duty Cycle  $\leq$  2%

 $<sup>^{3}</sup>$  Total Dose Irradiation with V<sub>GS</sub> Bias. V<sub>GS</sub> = 12V applied and V<sub>DS</sub> = 0 during irradiation per MIL-STD-750, Method 1019, condition A.

<sup>&</sup>lt;sup>4</sup> Total Dose Irradiation with  $V_{DS}$  Bias.  $V_{DS}$  = 24V applied and  $V_{GS}$  = 0 during irradiation per MlL-STD-750, Method 1019, condition A.

<sup>&</sup>lt;sup>5</sup> Part numbers IRHY57Z30CM (JANSR2N7482T3), IRHY53Z30CM (JANSF2N7482T3) and IRHY54Z30CM (JANSG2N7482T3)

### Radiation Hardened Power MOSFET Thru-Hole (TO-257AA)



**Device Characteristics** 

# 2.4.2 Single Event Effects – Safe Operating Area

IR HiRel radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. 1 and Table 7.

LET	Energy	Range			V <sub>DS</sub> (V)		
(MeV·cm²/mg)	(MeV)	(µm)	$V_{GS} = 0V$	$V_{GS} = -5V$	$V_{GS} = -10V$	$V_{GS} = -15V$	$V_{GS}$ = -20V
38 ± 5%	300 ± 7.5%	38 ± 7.5%	30	30	30	22.5	15
61 ± 5%	330 ± 7.5%	31 ± 10%	25	25	20	15	7.5
84 ± 5%	350 ± 10%	28 ± 7.5%	25	25	20	_	—

 Table 7
 Typical Single Event Effects Safe Operating Area

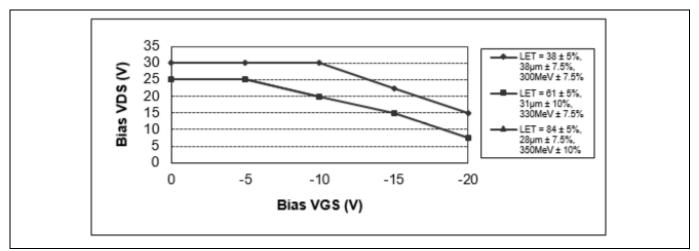


Figure 1 Typical Single Event Effect, Safe Operating Area

3

Radiation Hardened Power MOSFET Thru-Hole (TO-257AA)



**Electrical Characteristics Curves (Pre-irradiation)** 

Electrical Characteristics Curves (Pre-irradiation)

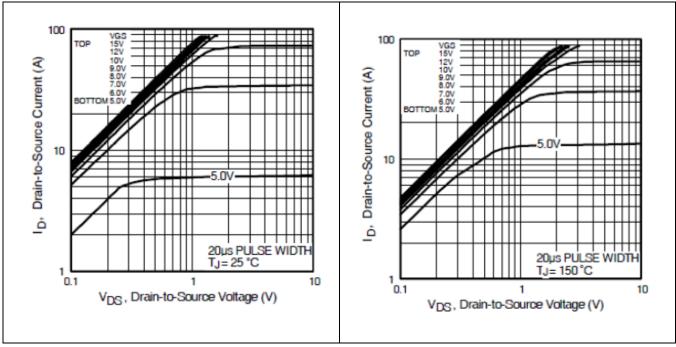
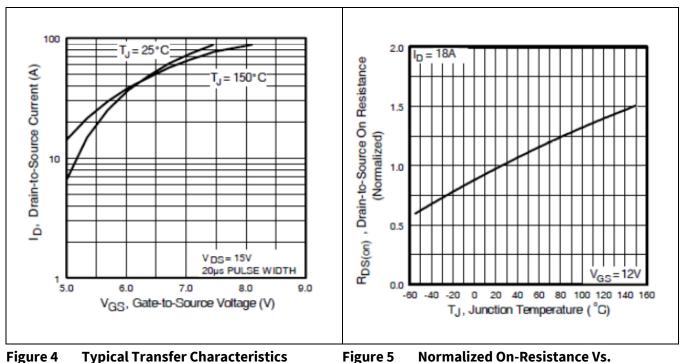


Figure 2 Typical Output Characteristics



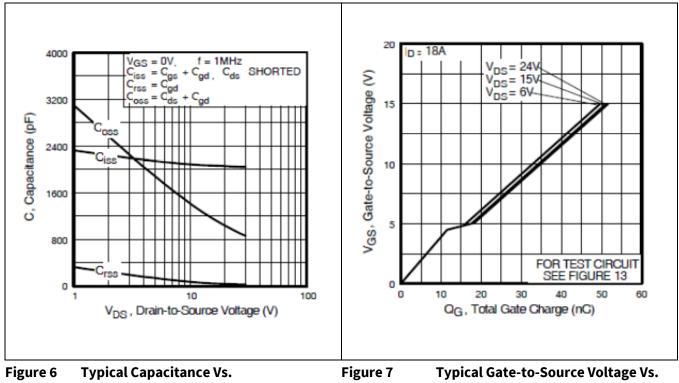


Temperature

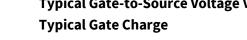


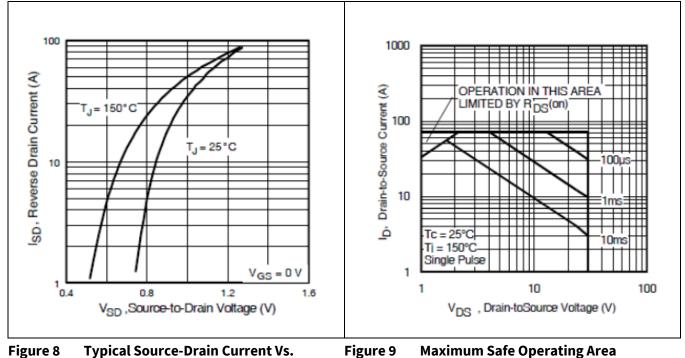


#### **Electrical Characteristics Curves (Pre-irradiation)**



**Drain-to-Source Voltage** 



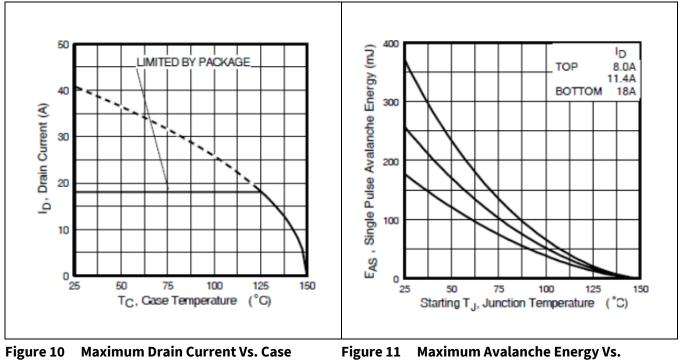


**Maximum Safe Operating Area** Figure 9





#### Electrical Characteristics Curves (Pre-irradiation)



Temperature

Junction Temperature

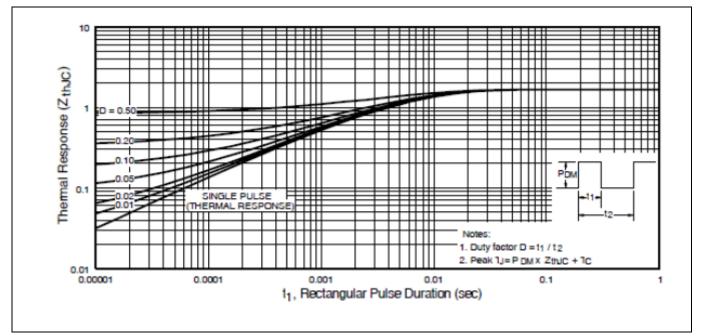


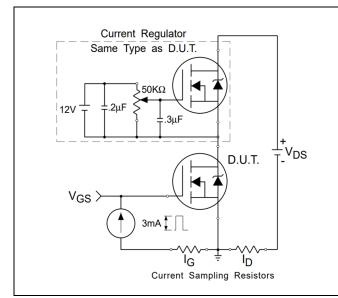
Figure 12 Maximum Effective Transient Thermal Impedance, Junction-to-Case

#### Radiation Hardened Power MOSFET Thru-Hole (TO-257AA)



**Test Circuits (Pre-irradiation)** 

4 Test Circuits (Pre-irradiation)





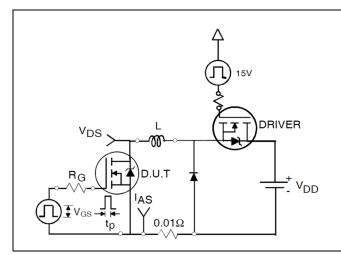
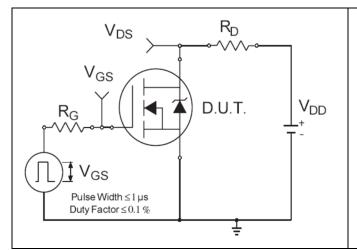
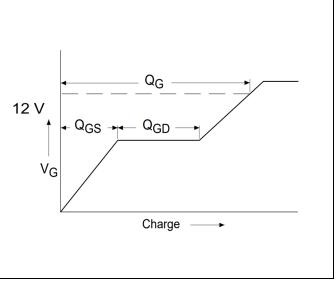
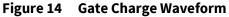


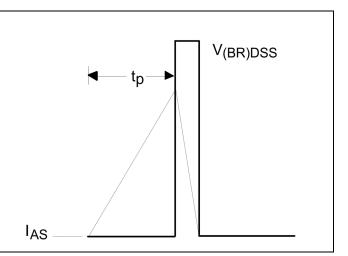
Figure 15 Unclamped Inductive Test Circuit













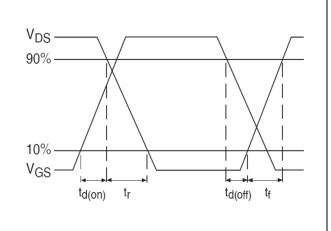


Figure 18 Switching Time Waveforms

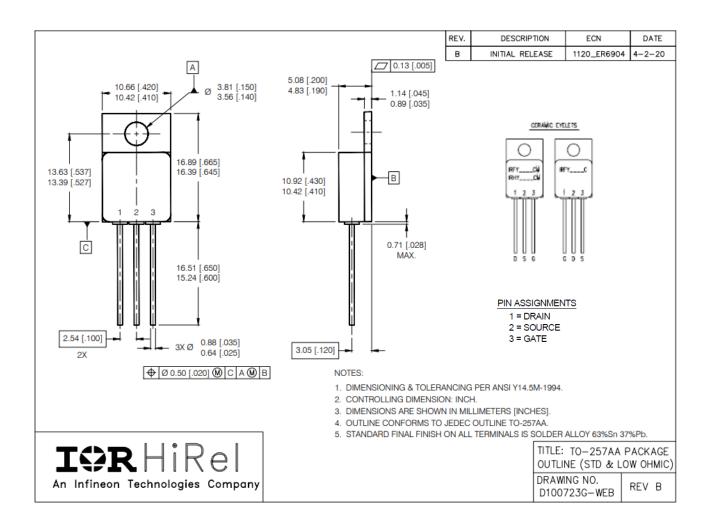
#### Radiation Hardened Power MOSFET Thru-Hole (TO-257AA)



**Package Outline** 

# 5 Package Outline

#### Note: For the most updated package outline, please see the website: TO-257AA



#### **BERYLLIA WARNING PER MIL-PRF-19500**

Package containing beryllia shall not be ground, sandblasted, machined, or have other operations performed on them which will produce beryllia or beryllium dust. Furthermore, beryllium oxide packages shall not be placed in acids that will produce fumes containing beryllium.



# **Revision history**

Document version	Date of release	Description of changes
	01/26/2000	Datasheet (PD-93824)
Rev A	04/17/2001	Updated switch time test condition
Rev B	07/18/2002	Updated IDSS max for 1000KRad(si)
Rev C	05/07/2004	Added QPL part number
Rev D	10/19/2004	Updated title from IRHY57034CM to IRHY57Z30CM
Rev E	04/25/2006	Updated 600kRad(si) to 500kRad(si)
Rev F	11/10/2020	Updated based on ECN-1120_08235
Rev G	05/27/2022	Updated based on ECN-1120_09018

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