

# Industrial DC/DC CONVERTER MGDDI-60 Ultra Wide Input: 60W POWER

Industrial Grade ■

# 12:1 Ultra Wide Input Dual Outputs Metallic Case - 2 250 VDC Isolation

- Ultra wide input range
- Nominal power up to 60 W
- . Nominal dual output voltage from 5V to 48V
- High efficiency over the entire range (typ. 91%)
- Soft start
- Galvanic isolation 2 250 VDC
- Integrated LC input filter
- Permanent short circuit protection
- External trim adjustment : -20/+10%
- No optocoupler for high reliability
- RoHS process



#### 1-General

The MGDDI-60 ultra wide input series designates a full family of DC/DC power modules designed for use in distributed power architecture where variable input voltage for multiple battery and transient are prevalent making them ideal particularly for transportation/mass transit, railways or high-end industrial applications. These modules use a high frequency fixed swiching topology at 270KHz providing excellent reliability, low noise characteristics and very high efficiency over the entire input voltage range.

Standard models are available with a permanent ultra wide input voltage range of 12-160 volts fully compatible with 24V/36V/72V/110V/125V battery applications. The serie includes dual output voltage choices individually isolated of 2 x 5 volts , 2 x 12 volts, 2 x 15 volts and 2 x 24 volts with easy configurability.

The MGDDI-60 serie is designed in conformity with safety standards EN60950.

All the modules are designed with LC network filters to minimize reflected input current ripple according to ease EN55022 standard.

The modules include a soft-start, an input undervoltage lock-out, a permanent short circuit protection, a thermal protection and an output overvoltage limitation to ensure efficient module protections. The soft-start allows current limitation and eliminates inrush current during start-up. The short circuit protection completely protects the modules against short-circuits of any duration by a shut-down and restores to normal when the overload is removed.

#### 2-Product Selection



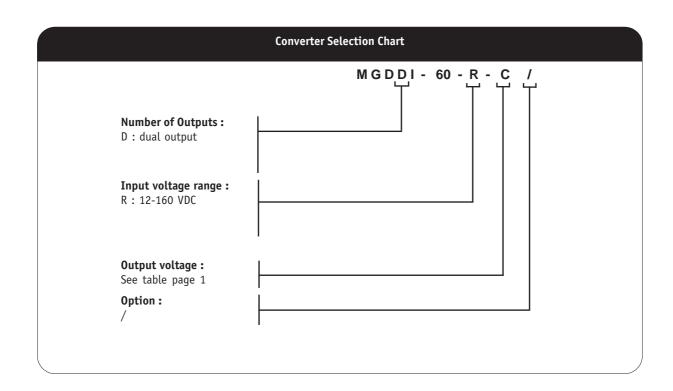


# 2- Product Selection (continued)

| Input range  | Output  | Current per Output            | Reference  | Options     |
|--|---|-------------------------------|--|-------------|
| 12-160 VDC<br>12-160 VDC<br>12-160 VDC<br>12-160 VDC | 2 x 5 VDC<br>2 x 12 VDC<br>2 x 15 VDC<br>2 x 24 VDC | 5 A<br>2,5 A<br>2 A<br>1,25 A | MGDDI-60-R-C<br>MGDDI-60-R-E<br>MGDDI-60-R-F<br>MGDDI-60-R-I | /<br>/<br>/ |

Using various parallel or series connections of outputs, and the 80/110% trim capability, allows to cover almost the complete range of output voltages from 4V to 52V as shown in the table below.

| Reference    | Parallel Connection | Series Connection |
|--------------|---------------------|-------------------|
| MGDDI-60-R-C | 4 - 5,5 VDC         | 8 - 11 VDC        |
| MGDDI-60-R-E | 9,6 - 13,2 VDC      | 19,2 - 26,4 VDC   |
| MGDDI-60-R-F | 12 - 16,5 VDC       | 24 - 33 VDC       |
| MGDDI-60-R-I | 19,2 - 26,4 VDC     | 38,4 - 52,8 VDC   |







# 3- Electrical Specifications

Data are valid at +25°C, unless otherwise specified.

| Parameter  | Conditions  | Limit or<br>typical                      | Units                        | Dual Output<br>MGDDI-60 - R         |
|--|---|--|------------------------------|-------------------------------------|
| Input  |   |  |                              |                                     |
| Nominal input voltage  | Full temperature range  | Nominal                                  | VDC                          | 48                                  |
| Permanent input<br>voltage range (Ui)  | Full temperature range<br>Full load see note (1)                    | Min Max.                                 | VDC                          | 12-160                              |
| transient Input Voltage  | Full temperature range<br>Full load                                 | Maximum                                  | VDC/ms                       | 165/1000<br>176/100                 |
| Undervoltage lock-out<br>(UVLO)  | Turn-on voltage (pin Uvlo open)<br>Turn-off voltage (pin Uvlo open) | Maximum<br>Maximum                       | VDC<br>VDC                   | 11,8<br>10,8                        |
| Start up time  | Ui nominal within 3 ms<br>Nominal output<br>Full load : resistive   | Maximum                                  | ms                           | 30                                  |
| Reflected ripple current   | Ui nominal, full load at<br>switching freq. BW = 20MHz              | Maximum                                  | % Inom.                      | 10                                  |
| No load input power  | Ui nominal<br>No load   | Maximum                                  | W                            | 1                                   |
| Standby input power  | Ui nominal  | Maximum                                  | W                            | 1                                   |
| Output   |   |  |                              |                                     |
| Output voltage   | Full temperature range<br>Ui min. to max.<br>75% load               | Nominal<br>Nominal<br>Nominal<br>Nominal | VDC<br>VDC<br>VDC<br>VDC     | 2 x 5<br>2 x 12<br>2 x 15<br>2 x 24 |
| Set Point accuracy   | Ambient temperature : +25°c<br>Ui nominal, 75% load                 | Maximum                                  | %                            | +/- 2                               |
| Total output power with both outputs   | Full temperature range<br>Ui min. to max.                           | Maximum                                  | W                            | 60                                  |
| Output current per output<br>5V output<br>12V output<br>15V output                           | Full temperature range Full load see note (1) Ui min. to max.       | Nominal<br>Nominal<br>Nominal            | A<br>A<br>A                  | 5,0<br>2,5<br>2,0                   |
| 24V output Unbalanced output   | Minimum load on Vo1<br>for Vo2 proper operation                     | Nominal<br>Typical                       | W W                          | 1,25<br>6                           |
| Ripple output voltage ** 5V output 12V output 15V output 24V output                          | Ui nominal<br>Full load<br>BW = 20MHz                               | Maximum<br>Maximum<br>Maximum<br>Maximum | mVpp<br>mVpp<br>mVpp<br>mVpp | 200<br>240<br>300<br>520            |
| Output regulation *<br>(Line + load + thermal)   | Ui min. to max.<br>0% to full load                                  | Maximum                                  | %                            | +/- 1,5                             |
| Cross load output regulation   | Ui min. to max.<br>V01 at nominal load<br>V02 from 25% to full load | Maximum                                  | %                            | +/-2                                |
| Output voltage trim  | As a function of output voltage                                     | Minimum<br>Maximum                       | %<br>%                       | 80<br>110                           |
| Maximum admissible<br>capacitive load<br>5V output<br>12V output<br>15V output<br>24V output | Ui nominal<br>Full load<br>Per output                               | Maximum<br>Maximum<br>Maximum<br>Maximum | µF<br>µF<br>µF<br>µF         | 2 200<br>820<br>680<br>470          |
| <u> </u>   | Ui nominal  |  | •                            |                                     |

Note: Modules have to be mounted with heatsink for electrical tests..

Note (1): From 12V to 14V and from 140V to 160V a derating of 66% of total maximum permanent power should be observed.

Note \*: Regulation is measured with both outputs in parallel configuration.

Note \*\*: The ripple output voltage is the periodic AC component imposed on the output voltage, an aperiodic and random component (noise) has also to be considered. This noise can be reduced by adding 1 external decoupling capacitor connected between Gin and Gout. These capacitance should be layed-out as close as possible from the converter. The ripple output voltage is measured by connecting a ceramic chip capacitor Co accross Vo and Go pins (C=100µF if Vo<5Vdc C=10µF if Vo>5Vdc).



# MGDDI-60 Ultra Wide Input Series



# 4- Switching Frequency

| Parameter           | Conditions  | Limit or typical | Specifications |
|---------------------|---|------------------|----------------|
| Switching frequency | Full temperature range<br>Ui min. to max.<br>No load to full load | Nominal, fixed   | 270 KHz        |

#### 5- Isolation

| Parameter                   | Conditions                                      | Limit or typical   | Specifications       |
|-----------------------------|---|--------------------|----------------------|
| Electric strength voltage * | Input to output                                 | Functional         | 2 250 VDC            |
| (Case not connected)        | Between outputs                                 | Functional         | 300 VDC              |
| Isolation resistance        | Input to case 500 VDC<br>Output to case 500 VDC | Minimum<br>Minimum | 100 M0hm<br>100 M0hm |

<sup>\*</sup>DC-DC converters are defined as component power supplies. As components they cannot comply with the provisions of any safety requirements without global conditions of acceptability (mechanical, conductors, .....). GAIA Converter DC/DC converter provides functional input-ouput insulation and functional insulation between outputs. The customer will be responsible to sustain all relevant end-user safety standards and regulations (IEC/EN/UL 60950-1).

#### 6- Protection Functions

| Characteristics                            | Protection Device                               | Recovery           | Limit or typical                    | Specifications         |
|--|---|--------------------|-------------------------------------|------------------------|
| Input undervoltage lock-out (UVLO)         | Turn-on, turn-off circuit with hysteresis cycle | Automatic recovery | Turn-on nominal<br>Turn-off nominal | See section 3          |
| Output current limitation protection (OCP) | Straight line current limitation                | Automatic recovery | Typical                             | 130%                   |
| Output overvoltage protection (OVP)        | Overvoltage protection limitation               | Automatic recovery | Nominal                             | 130% of output voltage |
| Over temperature protection (OTP)          | Thermal device with hysteresis cycle            | Automatic recovery | Nominal                             | 120°C                  |

# 7- Reliability Data

| Characteristics  | Conditions         | Temperature                     | Specifications             |
|--|--------------------|---------------------------------|----------------------------|
|  | Ground Benign      | Case at 40°C                    | 1 660 000 Hrs              |
| Mean Time Between Failure (MTBF)<br>According to MIL-HDBK-217F | Ground fixed (Gf)  | Case at 40°C<br>Case at 70°C    | 490 000 Hrs<br>225 000 Hrs |
|  | Ground mobile (Gm) | Case at 40°C<br>Case at 70°C    | 245 000 Hrs<br>110 000 Hrs |
| Mean Time Between Failure (MTBF)<br>According to IEC-62380-TR  | Railway, Payphone  | Ambient at 25°C<br>100% time on | 295 000 Hrs                |



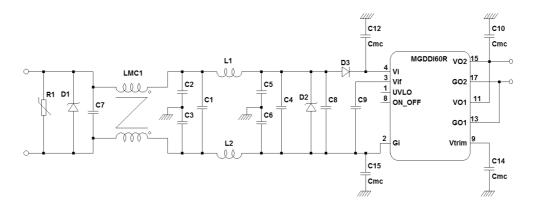


# 8- Electromagnetic Interference

Electromagnetic interference requirements according to EN55022 class A and class B can be easily achieved as indicated in the following table and following schematics:

|                   | Electromagnetic Interference according to EN55022 |   |  |  |  |  |
|-------------------|---|---|--|--|--|--|
| Conducted noise   | Configuration Models                              | With a common mode capacitor C $_{\rm c}$ = 10nF and external filter  |  |  |  |  |
| emission          | All models  | Class B   |  |  |  |  |
| Radiated<br>noise | Configuration Models                              | With a common mode capacitor C $_{\rm c}$ = 10 nF and external filter |  |  |  |  |
| emission          | All models  | Class B   |  |  |  |  |

#### 8-1 Module Compliance with EN55022 class A/class B Standard

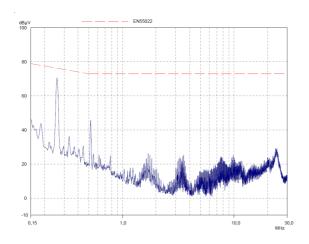


This filter includes 2 functions: an EMI filter part and a surge protection part. Please consult design note "EN50155 Transient/EMI Filter" for further details on this filter.

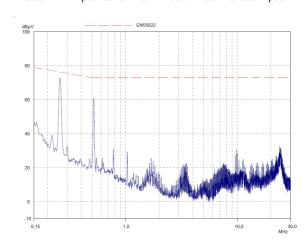
Note \* : C10,C12 to C15 are common mode noise capacitance Cmc

Note \*\*: transorb and diodes are adequate when surges are specified (see surge susceptibility section on following page).

#### EN55022 EMI profile for MGDDI-60-R-E at 72 Vdc input



#### EN55022 EMI profile for MGDDI-60-R-E at 110 Vdc input





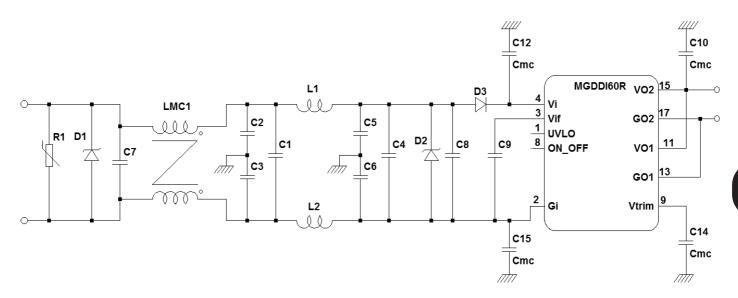


# 9- Surge Susceptibility EN61000-4-5 & EN50155

Surge susceptibility requirements according to EN50155, EN61000-4-5 can easily be achieved using:

• an input limitor filter : see following schematics of discret components, to sustain the following surge levels :

| Characteristics         | Standards    | Levels  |
|-------------------------|--------------|---|
| Spikes                  | EN 61000-4-5 | Level 4 with 4 000 V waveform 50 μs, impedance 2 0hm  |
| Line to line            | EN 50155     | Level 1 800 V waveform 50 μs, impedance 100 and 5 0hm<br>Level 8 400 V waveform 0.1 μs, impedance 100 0hm |
| Spikes<br>Line to earth | EN 61000-4-5 | Level 4 with 4 000 V waveform 50 μs, impedance 12 0hm   |
|                         | EN 50155     | Level 1 800 V waveform 50 μs, impedance 100 and 5 0hm<br>Level 8 400 V waveform 0.1 μs, impedance 100 0hm |



Please consult design note "EN50155 Transient/EMI Filter" for further details on components.



# MGDDI-60 Ultra Wide Input Series



#### 10- Thermal Characteristics

| Characteristics                               | Conditions   | Limit or typical   | Performances                  |
|---|--|--------------------|-------------------------------|
| Operating ambient temperature range           | Ambient temperature                                | Minimum<br>Maximum | - 40°C<br>see below           |
| Operating case temperature range at full load | Case temperature                                   | Minimum<br>Maximum | - 40°C<br>see curves herafter |
| Storage temperature range                     | Non functionning                                   | Minimum<br>Maximum | - 40°C<br>+ 115°C             |
| Thermal resistance                            | Rth case to ambient in free air natural convection | Typical            | 7,5°C /W                      |

The following discussion will help designer to determine the thermal characteristics and the operating temperature.

Heat can be removed from the baseplate via three basic mechanisms :

- Radiation transfert: radiation is counting for less than 5% of total heat transfert in majority of case, for this reason the presence of radient cooling is used as a safety margin and is not considered.
- Conduction transfert: in most of the applications, heat will be conducted from the baseplate into an attached heatsink or heat conducting member; heat is conducted thru the interface.
- Convection transfert : convecting heat transfer into air refers to still air or forced air cooling.

In majority of the applications, we will consider that heat will be removed from the baseplate either with:

- heatsink,
- forced air cooling,
- both heatsink and forced air cooling.

To calculate the maximum admissible ambient temperature the following method can be used.

Knowing the power used Pout and the efficiency  $\eta$ :

• determine the power dissipated by the module Pdiss that should be evacuated :

Pdiss = Pout
$$(1/\eta - 1)$$
 (A)

• then determine the thermal dissipation :

$$Tdiss = Rth(b-a) \times Pdiss$$
 (B)

where Rth(b-a) is the thermal resistance from the baseplate to ambient.

This thermal Rth(b-a) resistance is the summ of:

- the thermal resistance of baseplate to heatsink (Rth(b-h)). The interface between baseplate and heatsink can be nothing or a conducting member, a thermal compound, a thermal pad.... The value of Rth(b-h) can range from 0.4°C/W for no interface down to 0.1°C/W for a thermal conductive member interface.
- the thermal resistance of heatsink to ambient air (Rth(h-a)), which is depending of air flow and given by heatsink supplier.

The table hereafter gives some example of thermal resistance for different heat transfert configurations.

| Heat transfert        | t Thermal resistance heatsink to air Rth(h-a)                 |         | Thermal resistance baseplate to heatsink Rth(b-h) |          | Global<br>resistance |
|-----------------------|---|---------|---|----------|----------------------|
|                       | No Heatsink baseplate only :                                  | 7,5°C/W | No need of thermal pad                            |          | 7,5°C/W              |
| Free air cooling only | Heatsink Fischer Elektronik SK100 75SA                        | 2°C/W   | Bergquist Silpad*or<br>Schlegel OP-8100 :         | 0,13°C/W | 2,13°C/W             |
|                       | Heatsink Wakefield Thermal Solution 517-94AB (57.9 x 61 x 24) | 2°C/W   | Bergquist Silpad*or<br>Schlegel OP-8100 :         | 0,13°C/W | 2,13°C/W             |

Aavid Thermalloy is a heasink manufacturers. «Silpad»  $^{\circ}$  is a registered trademark of Bergquist. Note\*: Silpad performance are for Silpad 400 with pressure conditions of 50 Psi.





# 10- Thermal Characteristics (continued)

The two formulas (A) and (B) described in previous page:

• Pdiss = Pout $(1/\eta - 1)$  (A)

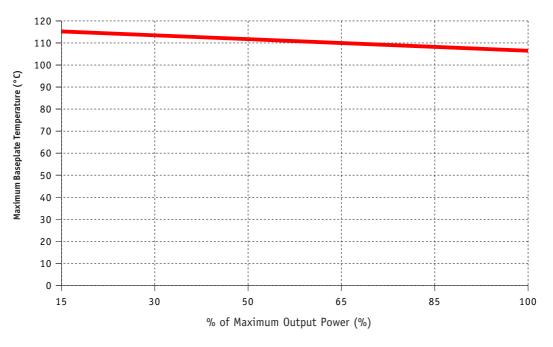
• Tdiss = Rth(b-a) x Pdiss (B)

conduct to determine the maximum ambient temperature admissible as a function of the maximum baseplate temperature of the module.

Knowing the maximum baseplate temparature  $\mathsf{Tmax}_{\mathsf{baseplate}}$  the maximum ambient temperature is given by the following formula :

 $Ta = Tmax_{baseplate} - Tdiss$  (C)

#### MGDDI-60 Series Maximum Baseplate Temperature Versus % of Maximum Output Power







# 11- Environmental Qualifications

The modules have been subjected to the following environmental qualifications.

| Characteristics                                | Conditions   | Severity  | Test procedure                               |
|--|--|---|--|
| Climatic Qualificat                            | ions   |   |  |
| Life at high<br>temperature                    | Duration<br>Temperature<br>Status of unit  | 1 000 Hrs<br>95°C case<br>unit operating  | IEC 68-2-2                                   |
| Humidity steady                                | Damp heat<br>Temperature<br>Duration<br>Status of unit   | 93 % relative humidity 40°C 56 days unit not operating  | IEC 68-2-3<br>Test Ca                        |
| Temperature<br>cycling                         | Number of cycles<br>Temperature change<br>Transfert time<br>Steady state time<br>Status of unit                          | 200<br>-40°C / +71°C<br>40 min.<br>20 min.<br>unit not operating  | IEC 68-2-14<br>Test N                        |
| Temperature<br>shock                           | Number of shocks Temperature change Transfert time Steady state time Status of unit                                      | 50 -40°C / +105°C 10 sec. 20 min. unit not operating  | IEC 68-2-14<br>Test Na                       |
| Mechanical Qualifi                             | cations  |   |  |
| Vibration<br>(Sinusoidal)                      | Number of cycles Frequency: amplitude Frequency: acceleration Amplitude /acceleration Duration Status of unit            | 10 cycles in each axis 10 to 60 Hz / 0.7 mm 60 to 2000 Hz / 10 g 0.7 mm/10 g 2h 30 min. per axis unit not operating | IEC 68-2-6<br>Test Fc                        |
| Shock<br>(Half sinus)                          | Number of shocks<br>Peak acceleration<br>Duration<br>Shock form<br>Status of unit  | 3 shocks in each axis 100 g 6 ms 1/2 sinusoidal unit not operating  | IEC 68-2-27<br>Test Ea                       |
| Bump<br>(Half sinus)                           | Number of bumps<br>Peak acceleration<br>Duration<br>Status of unit   | 2 000 bumps in each axis 25 g 6 ms unit not operating   | IEC 68-2-29<br>Test Eb                       |
| Electrical Immunit                             | y Qualifications   |   |  |
| Electrical<br>discharge<br>susceptibility      | Number of discharges<br>Air discharge level<br>Contact discharge level<br>Air discharge level<br>Contact discharge level | 10 positive & 10 negative discharges 4 kV : sanction A 2 Kk : sanction A 8 Kk : sanction B 4 kV : sanction B        | EN55082-2 with :<br>EN61000-4-2<br>IEC 801-2 |
| Electrical field<br>susceptibility             | Antenna position Electromagnetic field Wave form signal Frequency range  | at 1 m<br>10 V/m<br>AM 80%, 1 kHz<br>26 MHz to 1 GHz  | EN55082-2 with:<br>EN61000-4-3<br>IEC801-3   |
| Electrical fast<br>transient<br>susceptibility | Burst form<br>Wave form signal<br>Impedance<br>Level 1<br>Level 3  | 5/50 ns 5 kHz with 15 ms burst duration period 300 ms 50 0hm 0,5 kV : sanction A 2 kV : sanction B                  | EN55082-2 with :<br>EN61000-4-4<br>IEC801-4  |
| Surge<br>Susceptibility                        | Surge form<br>Impedance<br>Level 4   | 1,2/50 µs 2 Ohm 4 kV : with transient protection (see section surge)  | EN61000-4-5<br>EN50155                       |





# 12- Description of Protections

The MGDD-60 series includes 4 types of protection devices.

#### 12-1 Input Undervoltage Lockout (UVLO)

An input undervoltage protection will inhibit the module when input voltage drops below the lock-out turn-off threshold (see section 3 for value) and restores to normal operation automatically when the input voltage rises the lock-out turn-on threshold.

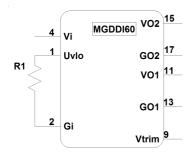
The UVLO voltage can be adjusted using a resistor (Ruvlo) connected between pin (1) and GIA. This value can be ajusted in order to allow converter to stops properly accordingly to the input bus (or battery) voltage value. The Ruvlo can be determined using the following formula:

Ruvlo  $(K\Omega) = [200.3-Vuvlo]/[Vuvlo-11.8]$ 

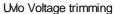
Ruvlo = trimming resistance

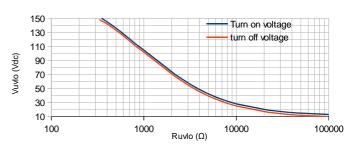
Vuvlo = desire turn-on voltage

Without resistor, the turn on voltage is 11.8V and turn off voltage is 10.8V



# On Off UVLO Turn-off Turn-on UVLO Turn-off Turn-on UVLO UVLO Turn-off Turn-on UVI O





#### 12-2 Output Over Current Limitation Protection (OCP)

The MGDD-60 Series incorporates a overcurrent protection circuit. The overcurrent protection detects short circuit or over current and protects the module according to the hiccup graph . The maximum detection current Id is depending on input voltage Vin, temperature, and is higher than 105 % maximum nominal output curent.

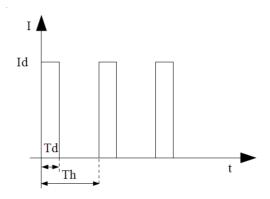
When OCP is triggered, the converter falls in hiccup mode by testing periodically if the overload is still present. The module restart automatically in soft-start to normal operation when overcurrent is removed. Td (detection time) and Th (hiccup period) are depending on Vin and temperature. In hiccup mode the average curent is arround 25 % of Inom.

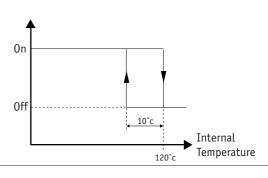
#### 12-3 Output Overvoltage Protection (OVP)

The MGDD-60 series has an internal overvoltage limitation protection circuit that monitors the voltage accross the output power terminals. It is designed to limit the converter output voltage to 130% (+/-10%) of nominal output voltage.

#### 12-4 Over Temperature Protection (OTP)

A thermal protection device adjusted at 120°C (+/-5%) internal temperature with 10°C hysteresis cycle will inhibit the module as long as the overheat is present and restores to normal operation automatically when overheat is removed. The efficiency of the OTP function is warranty with the module mounted on a heatsink.









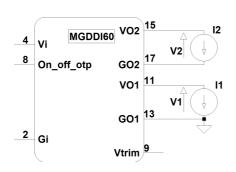
# 13- Description of Functions

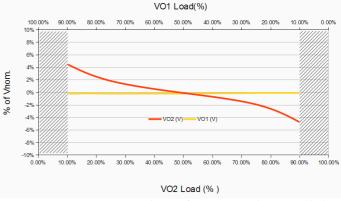
#### 13-1 Connection of Outputs

The outputs of MGDDI-60 can be connected in various configurations such as:

- connections in series
- connection in parallel
- connection in symmetry
- connection in independance

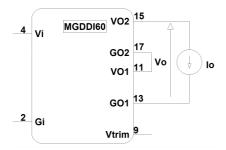
Please note that regulation is achieved through output V01/G01 referenced as primary output. When connected in symetrie or independant configurations with unbalanced loads, V01/G01 has to be loaded at 6W minimum to insure proper operating of the converter. The V02/G02 output referenced as secondary output may stay unloaded, but in that case its regulation may drift up as shown in curve below. There is no minimum load when the two outputs are connected in parallel or balanced serie.





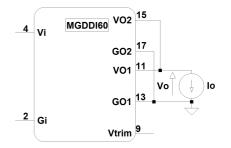
#### 13-1-1 Connection of Outputs in Series

Outputs connected in series allow to achieve 10V, 24V, 30V or 48V output voltages up to 60W total power. These values can be extended using trim adjustment.



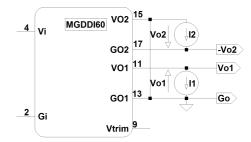
#### 13-1-2 Connection of Outputs in Parallel

Outputs connected in paralell allow to achieve single output 5V, 12V, 15V or 24V up to 60W power. These values can be extended using trim adjustment.



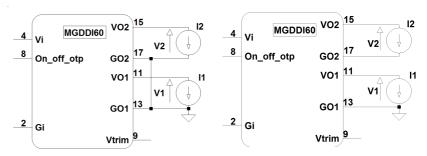
#### 13-1-3 Connection of Outputs in Symmetry

Outputs connected in symmetry allow to achieve +/-5V, +/-12V, +/-15V or +/-24V voltages (+/-30W each) with possible unbalanced loadup to 50W on primary output, 10W on secondary output and vice versa.



#### 13-1-4 Connection of Outputs in Independance

Outputs connected independantly with floating DC between each other can be achieved for 2x5V, 2x12V, 2x15V or 2x24V voltages (30W each) with possible unbalanced load up to 50W on primary output 10W on secondary output and vice versa.







# 13- Description of Functions (continued)

#### 13-2 Trim Function

The output voltage Vo1 may be trimmed in a range of 80%/110% of the nominal output voltage via a single external trimpot or fixed resistor.

The Vo2 output will automatically follow output Vo1.

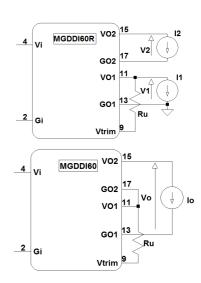
#### **Trim Up Function**

Do not attempt to trim the module higher than 110% of nominal output voltage as the overvoltage protection may occur.

Also do not exceed the maximum rated output power when the module is trimmed up.

The trim up resistance must be calculated with the following formula:

$$R_{u}(k \Omega) = \frac{(R1*VO_{nom}*(VO - V_{ref}))}{((VO - VO_{nom})*V_{ref})} - R1 - R2$$



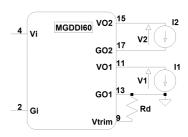
#### **Trim Down Function**

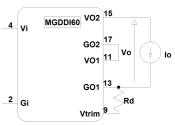
Do not trim down more than -20% of nominal output voltage otherwise the module may be damaged.

The available output power is reduced by the same percentage that output voltage is trimmed down.

The trim down resistance must be calculated with the following formula :

$$R_d(k\Omega) = \frac{((R1+R2)*VO - (R2*VO_{nom}))}{(VO_{nom} - VO)}$$





| Parameter           | Unit | Min. | Тур.  | Max. |
|---------------------|------|------|-------|------|
| Trim reference Vref | Vdc  | 1,2  | 1,225 | 1,25 |
| Resistor R1         | Ohm  | /    | 3,9K  | /    |
| Resistor R2         | Ohm  | /    | 270   | 1    |





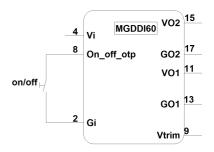
# 13- Description of Functions (continued)

#### 13-3 On/Off Function

The control pin 8 (On/Off) can be used for applications requiring On/Off operation. This may be done with an open collector transistor, a switch, a relay or an optocoupler. Several converters may be disabled with a single switch by connecting all On/Off pins together.

- The converter is disabled by pulling low the pin 8.
- No connection or high impedance on pin 1 enables the converter.

By releasing the On/Off function, the converter will restart within the start up time specifications given in table section 3

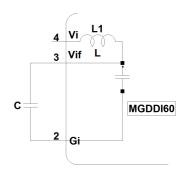


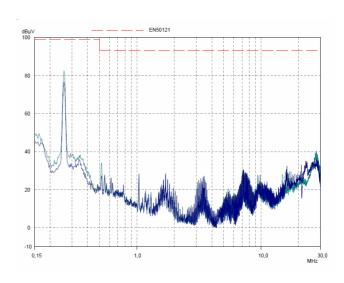
| Parameter                     | Unit | Min. | Тур. | Max. | Notes, conditions  |
|-------------------------------|------|------|------|------|--|
| On/Off module enable voltage  | Vdc  | 2.1  | /    | 3.3  | Open, the switch must not sink more than 50µA                    |
| On/Off module disable voltage | Vdc  | 0    | /    | 0.5  | The switch must be able to sink 0,5mA                            |
| On/Off alarm level            | Vdc  | 0    | /    | 0.5  | OTP faulty module  |
| On/Off module enable delay    | ms   | /    | /    | 30   | The module restarts with the same delay after alarm mode removed |
| On/Off module disable delay   | μs   | /    | /    | 100  | Vi nominal, full load  |

#### 13-4 Input Filter Compensation (VIF)

The «VIF» pin is a direct access to the capacitor of the LC input filter and allows to increase the C value to enhance the converter's stability and performance and to reduce the input current ripple for improved EMI performance.It is recommended to provide low ESR ceramic capacitors, total value can rate from  $2.2\mu F$  for high input voltage use to  $10\mu F$  for low input voltage use.These capacitors should have the proper voltage rating. Because of high current flowing through it, it should be connected between «VIF» and «Gin» as close as possible from the converter, using large copper traces.

The capacitor on Vif helps reducing conducted EMI level: the difference between bleue green curve beside highlights the effect of VIF capacitor.



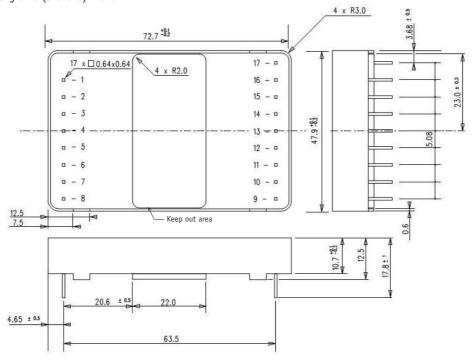






#### 14- Dimensions

Dimension are given in mm. Tolerance: +/- 0,2 mm (+/- 0.01 ") unless otherwise indicated. Weight: 100 grams (3.6 Ozs) max.



#### 15- Materials

Case: Metallic black anodized coating.

Pins: Flash gold plating over nickel underplate.

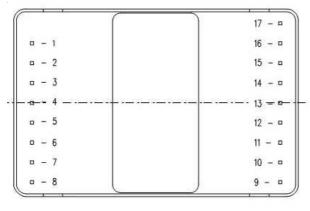
# 16- Product Marking

Upper face : Company logo.

Side face: Module reference, option, date code: year and week of manufacturing.

#### 17- Connections

The MGDDI-60 series has been designed for on-board mounting. it is recommended not to lay-out any component under the module.



Bottom view

| Pin     | Single           |
|---------|------------------|
| 1       | UVL0             |
| 2       | - Input (Gi)     |
| 3       | VIF              |
| 4       | + Input (Vi)     |
| 5, 6, 7 | Do not connect   |
| 8       | 0n/0ff           |
| 9       | Vtrim            |
| 10      | Do not connect   |
| 11      | + Output 1 (Vo1) |
| 12      | Do not connect   |
| 13      | - Output 1 (Go1) |
| 14      | Do not connect   |
| 15      | + Output 2 (Vo2) |
| 16      | Do not connect   |
| 17      | - Output 2 (Go2) |





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