

**SCHOTTKY RECTIFIER
HIGH EFFICIENCY SERIES**

30 Amp. 30V

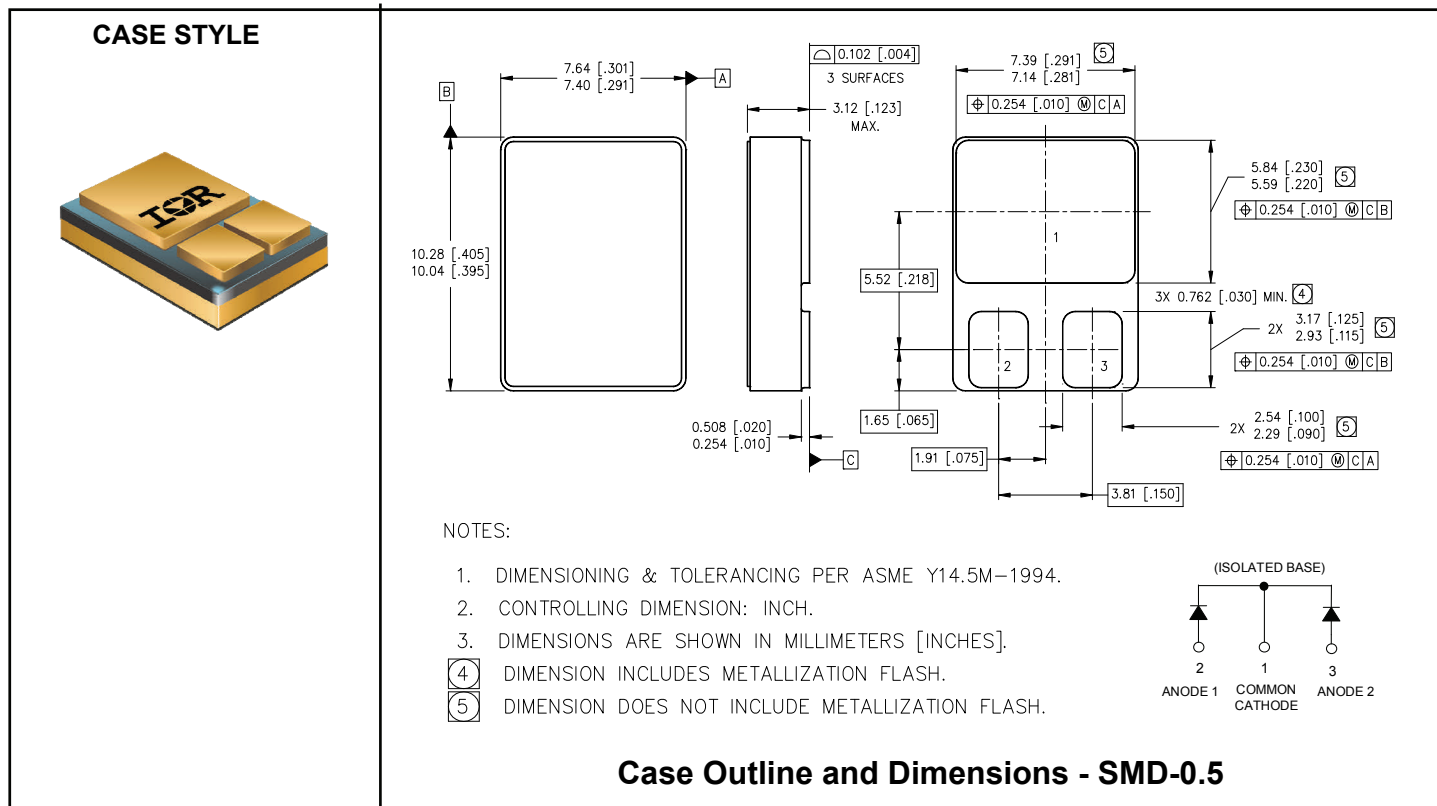
Major Ratings and Characteristics

Characteristics	30SCLJQ030	Units
$I_{F(AV)}$ (Per Leg)	30	A
V_{RRM} (Per Leg)	30	V
I_{FSM} @ $t_p = 8.3ms$ half-sine (Per Leg)	130	A
V_F @ 30Apk, $T_J = 125^\circ C$ (Per Leg)	0.82	V
T_J, T_{stg} Operating and storage	-55 to 150	$^\circ C$

Description/Features

The 30SCLJQ030 center tap Schottky rectifier has been expressly designed to meet the rigorous requirements of IR HiRel environments. It is packaged in the hermetic surface mount SMD-0.5 ceramic package. The device's forward voltage drop and reverse leakage current are optimized for the lowest power loss and the highest circuit efficiency for typical high frequency switching power supplies and resonant power converters. Full MIL-PRF-19500 quality conformance testing is available on source control drawings to TX, TXV and S quality levels.

- Hermetically Sealed
- Center Tap
- Low Forward Voltage Drop
- High Frequency Operation
- Guard Ring for Enhanced Ruggedness and Long term Reliability
- Surface Mount



Voltage Ratings

Part Number	30SCLJQ030
V_R Max. DC Reverse Voltage (V) (Per Leg)	30
V_{RRM} Max. Working Peak Reverse Voltage (V) (Per Leg)	

Absolute Maximum Ratings

Parameter	Limits	Units	Conditions
$I_{F(AV)}$ Max. Average Forward Current (Per Leg) See Fig. 5	30	A	50% duty cycle @ $T_C = 80^\circ\text{C}$, square waveform
I_{FSM} Max. Peak One Cycle Non - Repetitive Surge Current (Per Leg)	130	A	$t_p = 8.3$ ms half-sine

Electrical Specifications

Parameter	Limits	Units	Conditions	
V_{FM} Max. Forward Voltage Drop (Per Leg) See Fig. 1①	0.67	V	$I_F = 15\text{A}$	$T_J = -55^\circ\text{C}$
	0.85	V	$I_F = 30\text{A}$	
	0.61	V	$I_F = 15\text{A}$	$T_J = 25^\circ\text{C}$
	0.82	V	$I_F = 30\text{A}$	
	0.55	V	$I_F = 15\text{A}$	$T_J = 125^\circ\text{C}$
	0.82	V	$I_F = 30\text{A}$	
I_{RM} Max. Reverse Leakage Current (Per Leg) See Fig. 1①	0.27	mA	$T_J = 25^\circ\text{C}$	$V_R = \text{rated } V_R$
	17	mA	$T_J = 100^\circ\text{C}$	
	80	mA	$T_J = 125^\circ\text{C}$	
C_T Max. Junction Capacitance (Per Leg)	518	pF	$V_R = 5V_{DC}$ (1MHz, 25°C)	
L_S Typical Series Inductance (Per Leg)	4.8	nH	Measured from center of cathode pad to center of anode pad	

Thermal-Mechanical Specifications

Parameter	Limits	Units	Conditions
T_J Max. Junction Temperature Range	-55 to 150	$^\circ\text{C}$	
T_{stg} Max. Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
R_{thJC} Max. Thermal Resistance, Junction to Case (Per Leg)	3.5	$^\circ\text{C/W}$	DC operation See Fig. 4
R_{thJC} Max. Thermal Resistance, Junction to Case (Per Package)	1.75	$^\circ\text{C/W}$	DC operation
W_t Weight (Typical)	1.0	g	
Die Size (Typical)	70 x 92	mils	
Case Style	SMD-0.5		

 ① Pulse Width < 300 μs , Duty Cycle < 2%

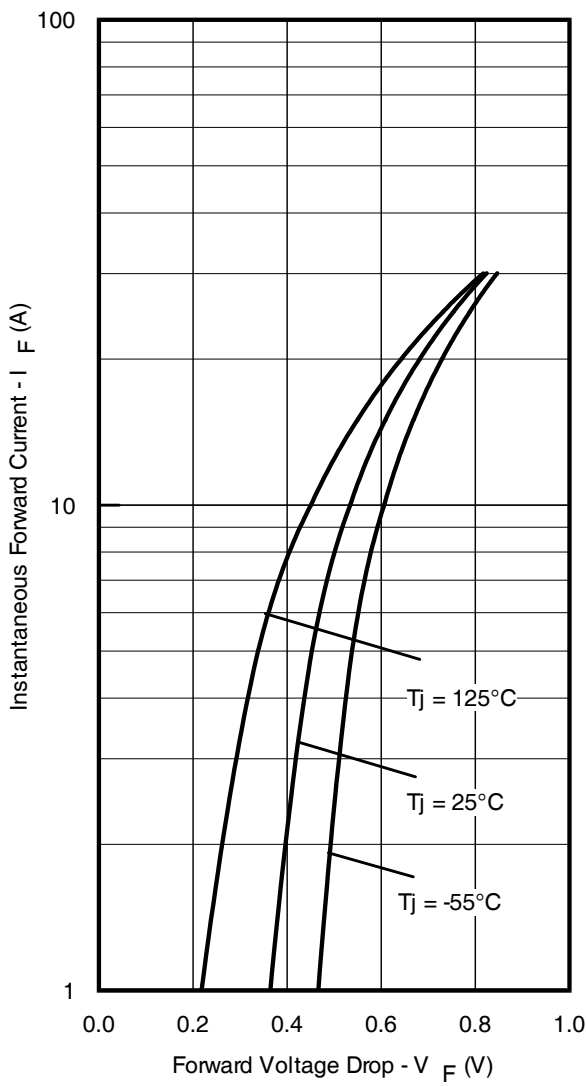


Fig 1. Max. Forward Voltage Drop Characteristics (Per Leg)

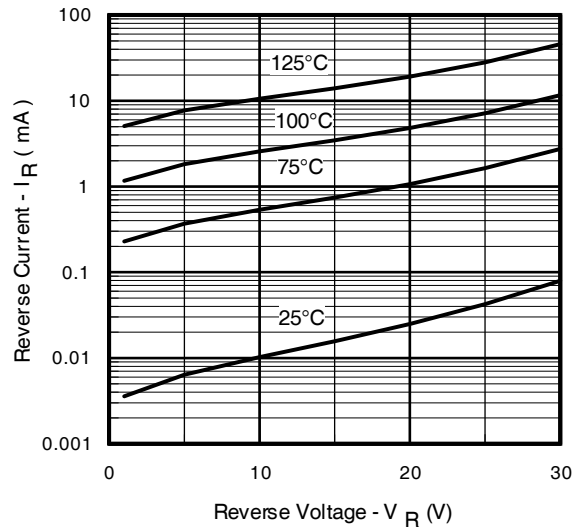


Fig 2. Typical Values of Reverse Current Vs. Reverse Voltage (Per Leg)

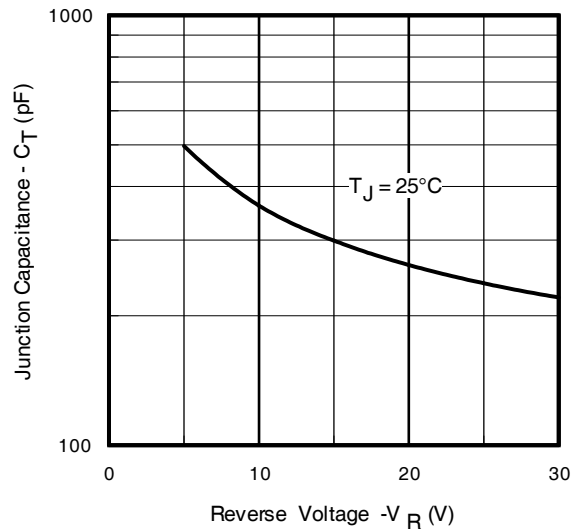


Fig 3. Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

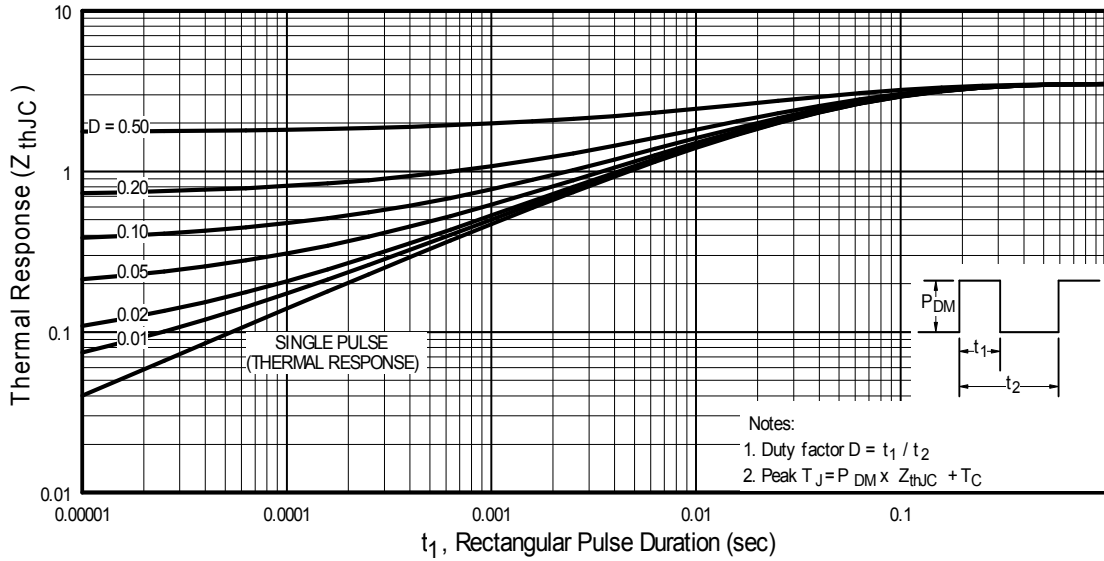


Fig 4. Max. Thermal Impedance Z_{thJC} Characteristics (Per Leg)

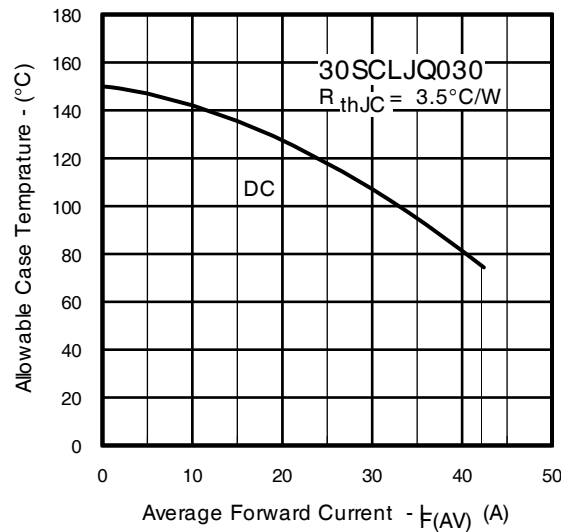


Fig 5. Max. Allowable Case Temperature Vs. Average Forward Current (Per Leg)

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