An Infineon Technologies Company

PD-91806D

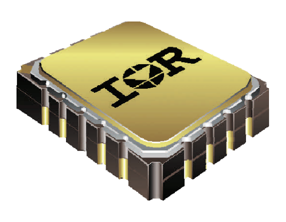
IRHE7130
JANSR2N7261U

RADIATION HARDENED POWER MOSFET SURFACE MOUNT (LCC-18)

100V, N CHANNEL
REF: MIL-PRF-19500/601
RAD Hard HEXFET TECHNOLOGY

Product Summary

Part Number	Radiation Level	RDS(on)	I_D	QPL Part Number
IRHE7130	100 kRads(Si)	0.18Ω	8.0A	JANSR2N7261U
IRHE3130	300 kRads(Si)	0.18Ω	8.0A	JANSF2N7261U
IRHE5130	500 kRads(Si)	0.18Ω	8.0A	JANSG2N7261U
IRHE8130	1000 kRads(Si)	0.18Ω	8.0A	JANSH2N7261U


LCC-18

Description

IR HiRel RAD-Hard HEXFET technology provides high performance power MOSFETs for space applications. This technology has over a decade of proven performance and reliability in satellite applications. These devices have been characterized for both Total Dose and Single Event Effects (SEE). The combination of low Rdson and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well established advantages of MOSFETs such as voltage control, fast switching and temperature stability of electrical parameters.

Features

- Single Event Effect (SEE) Hardened
- Low RDS(on)
- Low Total Gate Charge
- Simple Drive Requirements
- Hermetically Sealed
- Surface Mount
- Light Weight
- ESD Rating: Class 1C per MIL-STD-750, Method 1020

Absolute Maximum Ratings **Pre-Irradiation**

Symbol	Parameter	Value	Units
I_{D1} @ V_{GS} = 12V, T_C = 25°C	Continuous Drain Current	8.0	A
I_{D2} @ V_{GS} = 12V, T_C = 100°C	Continuous Drain Current	5.0	
I_{DM} @T_C = 25°C	Pulsed Drain Current ①	32	
P_D @T_C = 25°C	Maximum Power Dissipation	25	W
	Linear Derating Factor	0.20	W/°C
V_{GS}	Gate-to-Source Voltage	± 20	V
E_{AS}	Single Pulse Avalanche Energy ②	130	mJ
I_{AR}	Avalanche Current ①	8.0	A
E_{AR}	Repetitive Avalanche Energy ①	2.5	mJ
dv/dt	Peak Diode Recovery dv/dt ③	5.5	V/ns
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to + 150	°C
	Package Mounting Surface Temperature	300 (for 5s)	
	Weight	0.42 (Typical)	g

For Footnotes, refer to the page 2

Electrical Characteristics @ Tj = 25°C (Unless Otherwise Specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	100	——	——	V	V_{GS} = 0V, I_D = 1.0mA
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	——	0.10	——	V/°C	Reference to 25°C, I_D = 1.0mA
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	——	——	0.18	Ω	V_{GS} = 12V, I_{D2} = 5.0A ④
		——	——	0.185		V_{GS} = 12V, I_{D1} = 8.0A ④
$V_{GS(th)}$	Gate Threshold Voltage	2.0	——	4.0	V	V_{DS} = V_{GS}, I_D = 1.0mA
Gfs	Forward Transconductance	2.5	——	——	S	V_{DS} = 15V, I_{D2} = 5.0A ④
I_{DSS}	Zero Gate Voltage Drain Current	——	——	25	μA	V_{DS} = 80V, V_{GS} = 0V
		——	——	250		V_{DS} = 80V,V_{GS} = 0V,T_J =125°C
I_{GSS}	Gate-to-Source Leakage Forward	——	——	100	nA	V_{GS} = 20V
	Gate-to-Source Leakage Reverse	——	——	-100		V_{GS} = -20V
Q_G	Total Gate Charge	——	——	50	nC	I_{D1} = 8.0A
Q_{GS}	Gate-to-Source Charge	——	——	10		V_{DS} = 50V
Q_{GD}	Gate-to-Drain ('Miller') Charge	——	——	20		V_{GS} = 12V
$t_{d(on)}$	Turn-On Delay Time	——	——	25	ns	V_{DD} = 50V
tr	Rise Time	——	——	32		I_{D1} = 8.0A
$t_{d(off)}$	Turn-Off Delay Time	——	——	40		R_G = 2.35Ω
t_f	Fall Time	——	——	40		V_{GS} = 12V
Ls +L_D	Total Inductance	——	6.1	——	nH	Measured from the center of drain pad to center of source pad
C_{iss}	Input Capacitance	——	1100	——	pF	V_{GS} = 0V
C_{oss}	Output Capacitance	——	310	——		V_{DS} = 25V
C_{rss}	Reverse Transfer Capacitance	——	55	——		f = 1.0MHz

Source-Drain Diode Ratings and Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
I_S	Continuous Source Current (Body Diode)	——	——	8.0	A	
I_{SM}	Pulsed Source Current (Body Diode) ①	——	——	32		
V_{SD}	Diode Forward Voltage	——	——	1.5	V	T_J = 25°C,I_S = 8.0A, V_{GS} = 0V④
t_{rr}	Reverse Recovery Time	——	——	270	ns	T_J = 25°C, I_F = 8.0A, V_{DD} ≤ 50V
Q_{rr}	Reverse Recovery Charge	——	——	3.0	μC	di/dt = 100A/μs ④
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L_S+L_D)				

Thermal Resistance

Symbol	Parameter	Min.	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	——	——	5.0	°C/W
$R_{\theta J\text{-}PCB}$	Junction-to-PC Board	——	19	——	

Footnotes:

① Repetitive Rating; Pulse width limited by maximum junction temperature.

② V_{DD} = 25V, starting T_J = 25°C, L = 4.1mH, Peak I_L = 8.0A, V_{GS} = 12V

③ I_{SD} ≤ 8.0A, di/dt ≤ 140A/μs, V_{DD} ≤ 100V, T_J ≤ 150°C

④ Pulse width ≤ 300 μs; Duty Cycle ≤ 2%

⑤ Total Dose Irradiation with V_{GS} Bias. 12 volt V_{GS} applied and V_{DS} = 0 during irradiation per MIL-STD-750, Method 1019, condition A.

⑥ Total Dose Irradiation with V_{DS} Bias. 80 volt V_{DS} applied and V_{GS} = 0 during irradiation per MIL-STD-750, Method 1019, condition A.

Radiation Characteristics

IR HiRel Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at IR Hirel is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

Table1. Electrical Characteristics @ Tj = 25°C, Post Total Dose Irradiation ⑤⑥

Symbol	Parameter	100 kRads (Si)[1]		Up to 300k - 1000 kRads (Si)[2]		Units	Test Conditions
		Min.	Max.	Min.	Max.		
BV_{DSS}	Drain-to-Source Breakdown Voltage	100	——	100	——	V	V_{GS} = 0V, I_D = 1.0mA
$V_{GS(th)}$	Gate Threshold Voltage	2.0	4.0	1.25	4.5	V	V_{DS} = V_{GS}, I_D = 1.0mA
I_{GSS}	Gate-to-Source Leakage Forward	——	100	——	100	nA	V_{GS} = 20V
I_{GSS}	Gate-to-Source Leakage Reverse	——	-100	——	-100	nA	V_{GS} = -20V
I_{DSS}	Zero Gate Voltage Drain Current	——	25	——	50	µA	V_{DS} = 80V, V_{GS} = 0V
$R_{DS(on)}$	Static Drain-to-Source ④ On-State Resistance (TO-3)	——	0.18	——	0.24	Ω	V_{GS} = 12V, I_{D2} = 5.0A
$R_{DS(on)}$	Static Drain-to-Source ④ On-State Resistance (TO-39)	——	0.18	——	0.24	Ω	V_{GS} = 12V, I_{D2} = 5.0A
V_{SD}	Diode Forward Voltage ④	——	1.5	——	1.5	V	V_{GS} = 0V, I_S = 8.0A

1. Part numbers IRHE7130 (JANSR2N7261U)
2. Part numbers IRHE3130 (JANSF2N7261U) and IRHE5130(JANSG2N7261U), IRHE8130 (JANSH2N7261U)

IR HiRel radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

Table 2. Typical Single Event Effect Safe Operating Area

Ion	LET (MeV/(mg/cm^2))	Energy (MeV)	Range (µm)	VDS (V)				
				@VGS=0V	@VGS=-5V	@VGS=-10V	@VGS=-15V	@VGS=-20V
Cu	28	285	43	100	100	100	80	60
Br	36.8	305	39	100	90	70	50	——

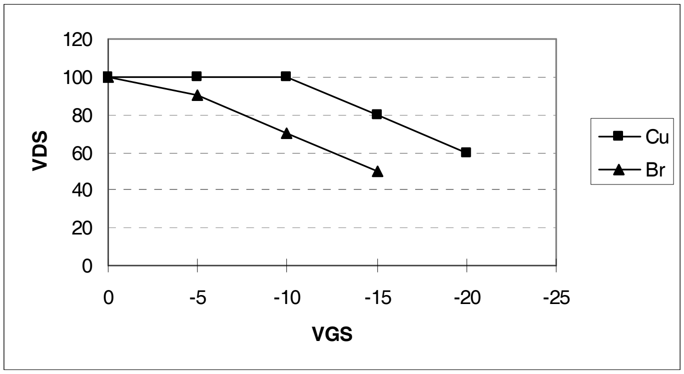


Fig a. Typical Single Event Effect, Safe Operating Area

For Footnotes, refer to the page 2

Pre-Irradiation

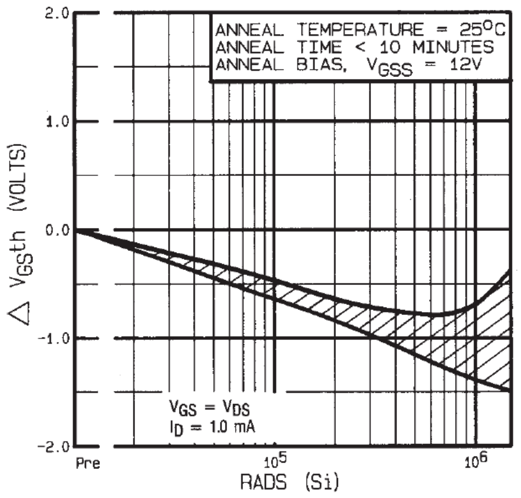


Fig 1. Typical Response of Gate Threshhold Voltage Vs. Total Dose Exposure

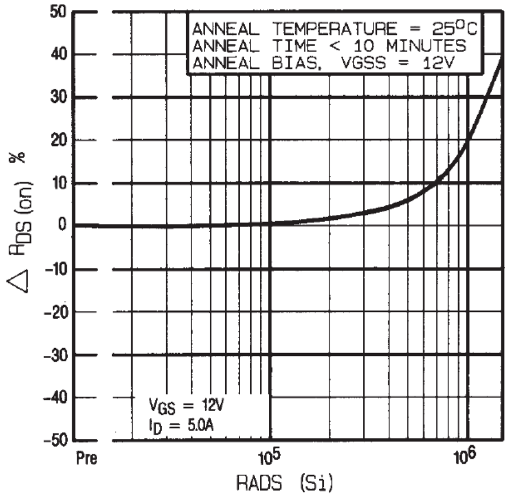


Fig 2. Typical Response of On-State Resistance Vs. Total Dose Exposure

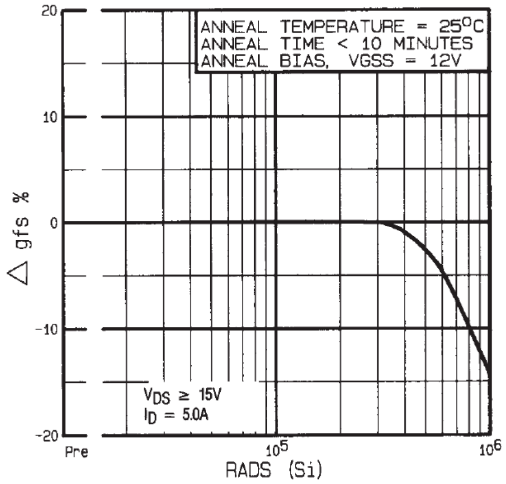


Fig 3. Typical Response of Transconductance Vs. Total Dose Exposure

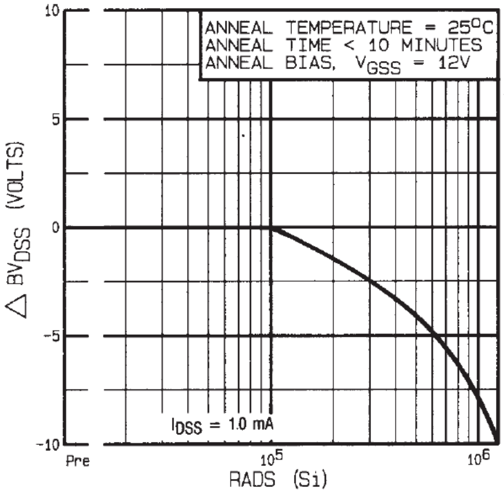


Fig 4. Typical Response of Drain to Source Breakdown Vs. Total Dose Exposure

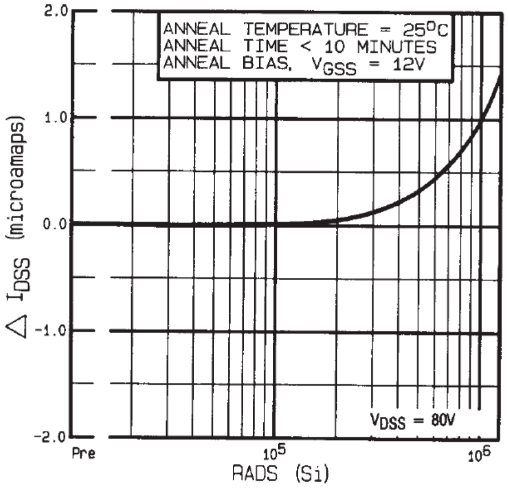


Fig 5. Typical Zero Gate Voltage Drain Current Vs. Total Dose Exposure

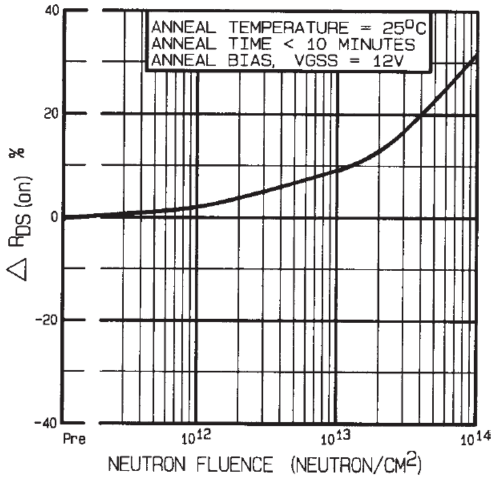


Fig 6. Typical On-State Resistance Vs. Neutron Fluence Level

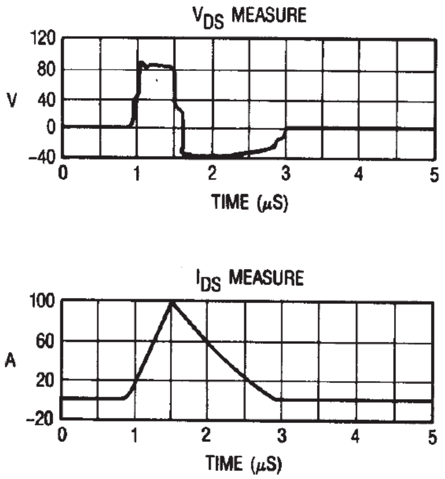


Fig 7. Typical Transient Response of Rad Hard HEXFET During 1x10^{12} Rad (Si)/Sec Exposure

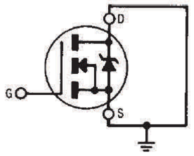


Fig 8a. Gate Stress of V_{GSS} Equals 12 Volts During Radiation

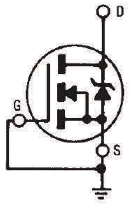


Fig 8b. V_{DSS} Stress Equals 80% of B_{VDSS} During Radiation

Note: Bias Conditions during radiation: V_{GS} = 12 Vdc, V_{DS} = 0 Vdc, Fig-9,10,11,12

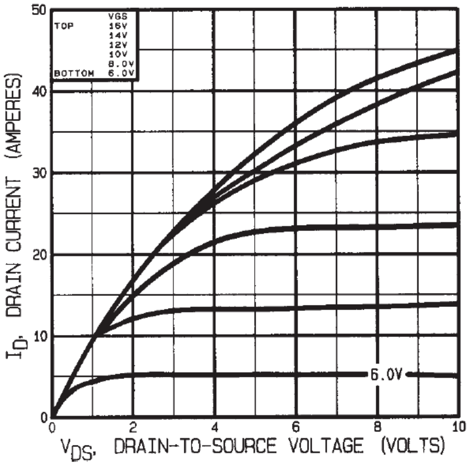


Fig 9. Typical Output Characteristics Pre-Irradiation

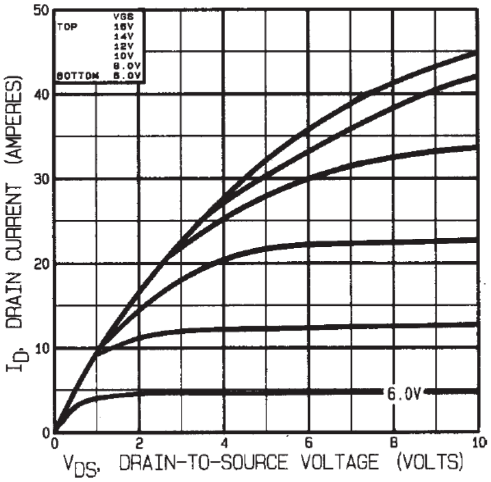


Fig 10. Typical Output Characteristics Post-Irradiation 100K Rads (Si)

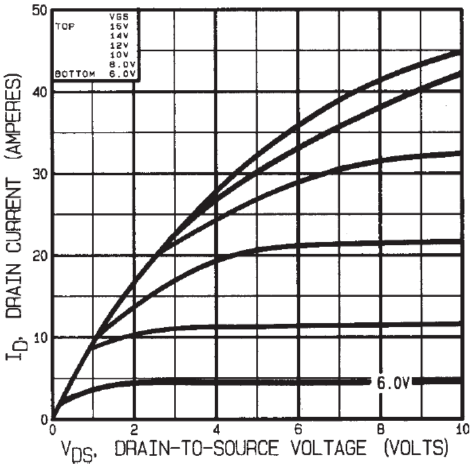


Fig 11. Typical Output Characteristics Post-Irradiation 300K Rads (Si)

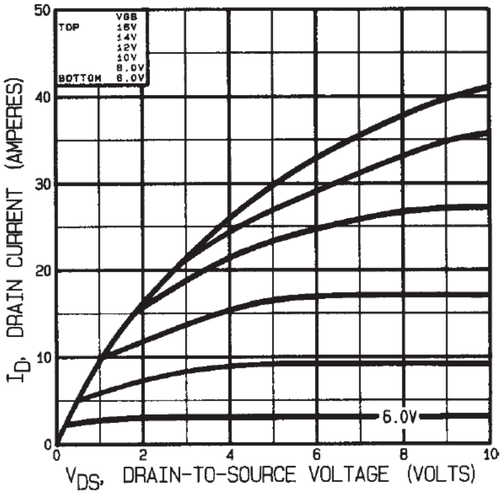


Fig 12. Typical Output Characteristics Post-Irradiation 1 Mega Rads (Si)

Note: Bias Conditions during radiation: V_{GS} = 0 Vdc, V_{DS} = 80 Vdc Fig 13,14, 15,16

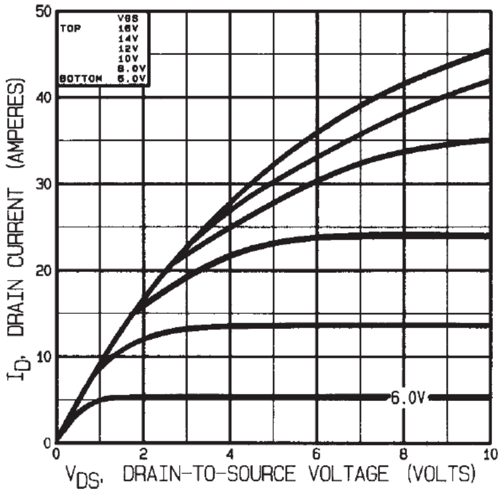


Fig 13. Typical Output Characteristics
Pre-Irradiation

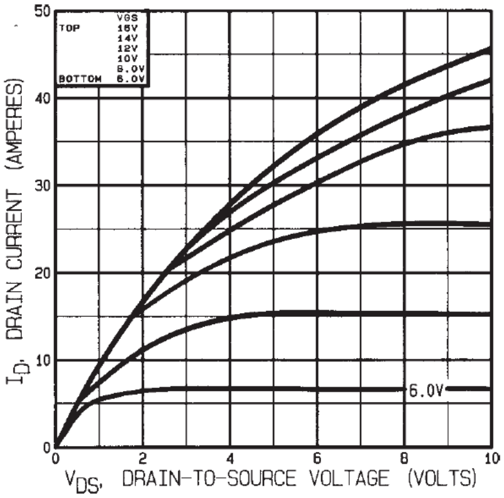


Fig 14. Typical Output Characteristics
Post-Irradiation 100K Rads (Si)

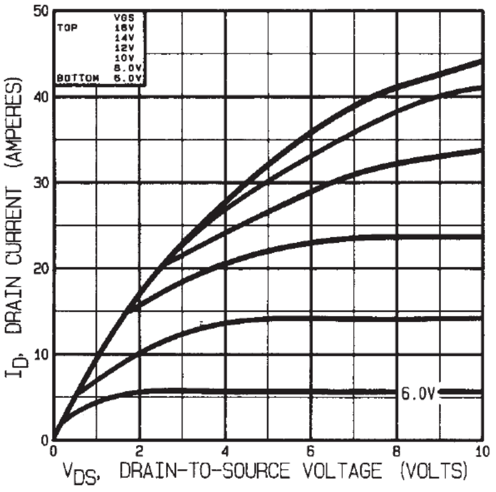


Fig 15. Typical Output Characteristics
Post-Irradiation 300K Rads (Si)

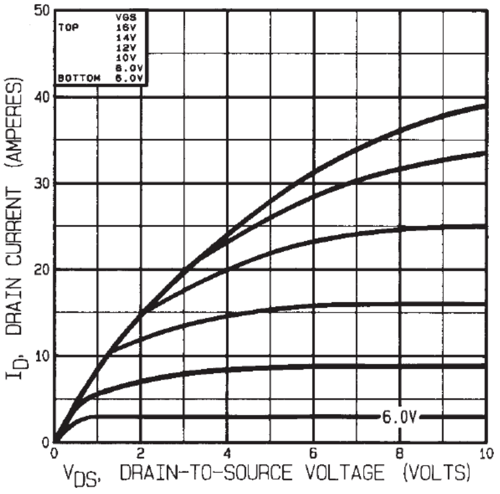


Fig 16. Typical Output Characteristics
Post-Irradiation 1 Mega Rads (Si)

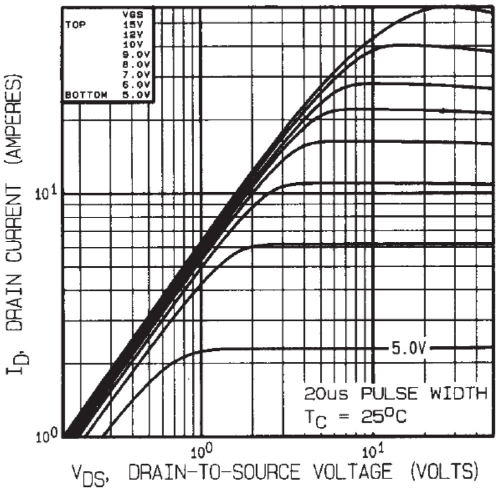


Fig 17. Typical Output Characteristics

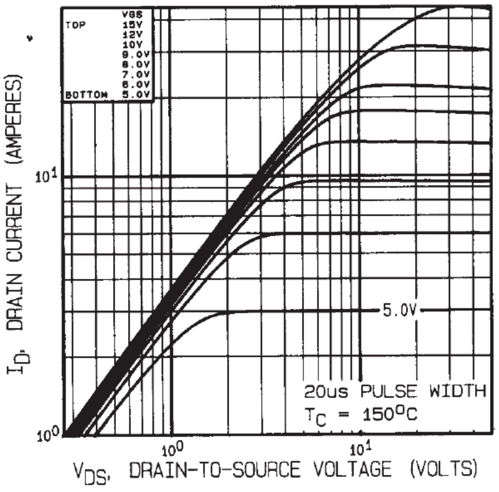


Fig 18. Typical Output Characteristics

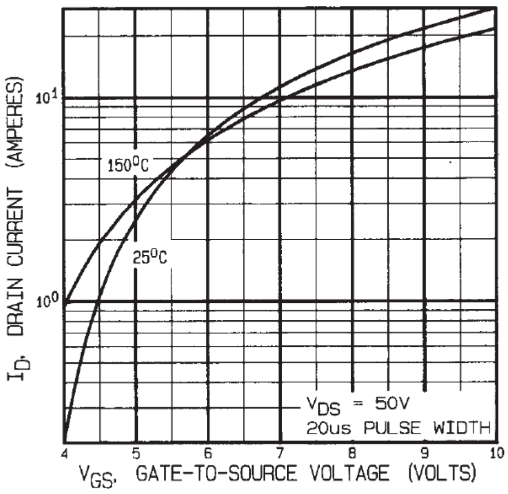


Fig 19. Typical Transfer Characteristics

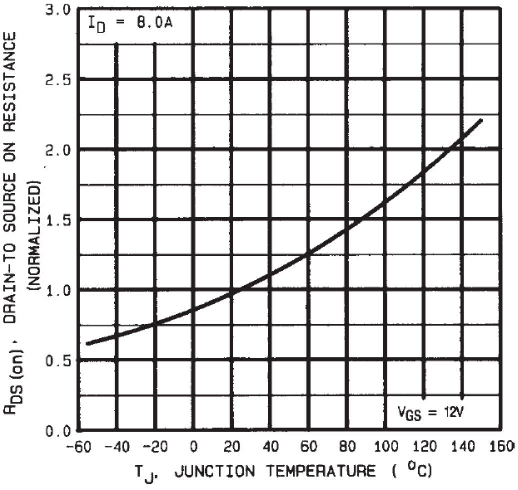


Fig 20. Normalized On-Resistance Vs. Temperature

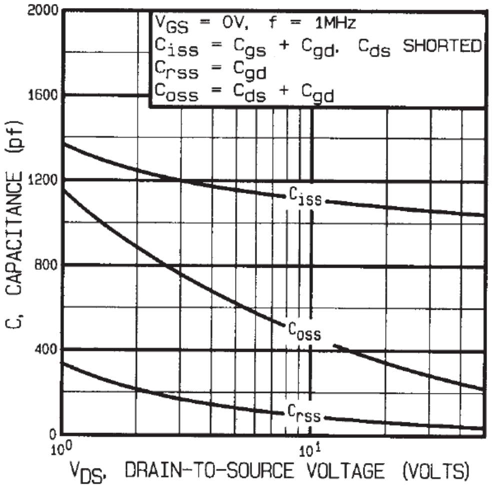


Fig 21. Typical Capacitance Vs. Drain-to-Source Voltage

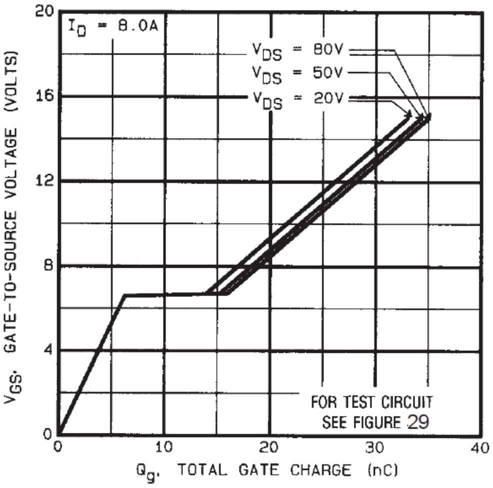


Fig 22. Typical Gate Charge Vs. Gate-to-Source Voltage

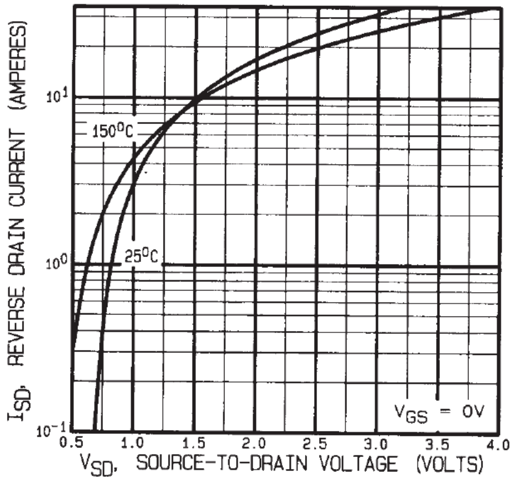


Fig 23. Typical Source-Drain Diode Forward Voltage

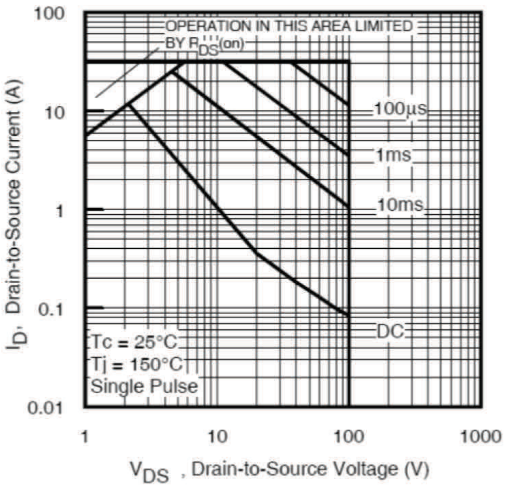


Fig 24. Maximum Safe Operating Area

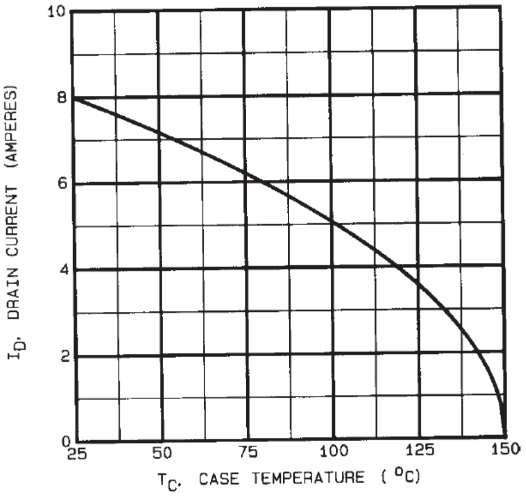


Fig 25. Maximum Drain Current Vs. Case Temperature

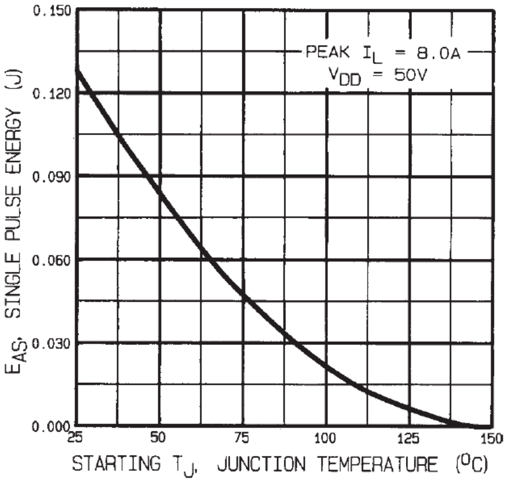


Fig 26. Maximum Avalanche Energy Vs. Drain Current

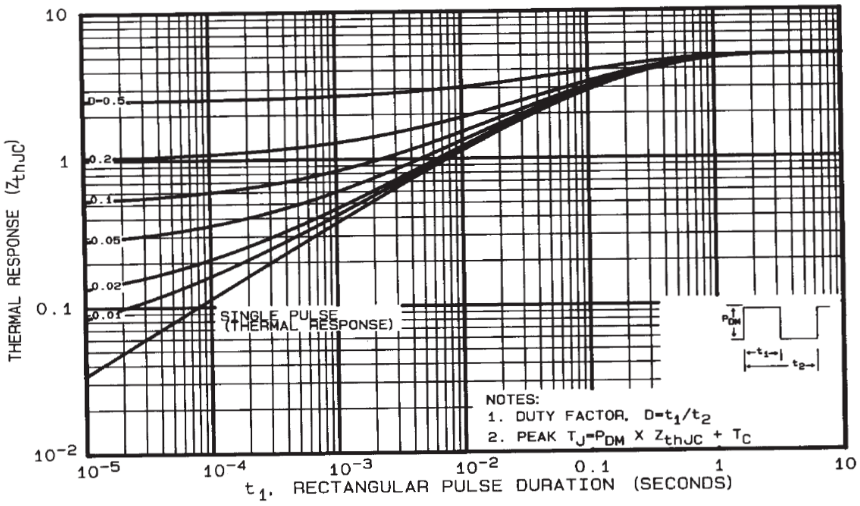


Fig 27. Maximum Effective Transient Thermal Impedance, Junction-to-Case

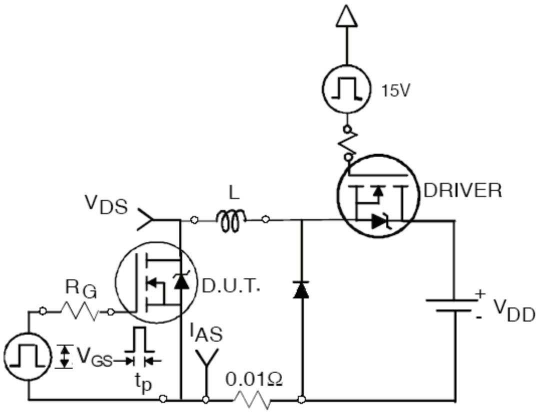


Fig 28a. Unclamped Inductive Test Circuit

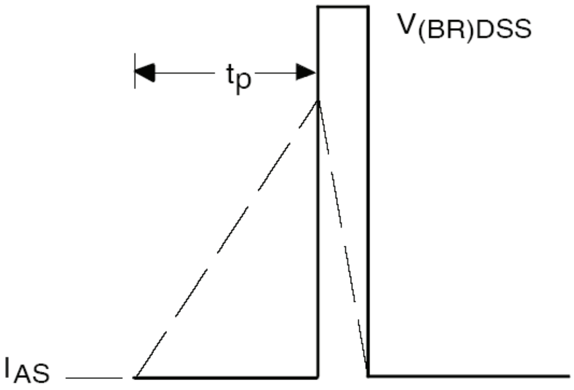


Fig 28b. Unclamped Inductive Waveforms

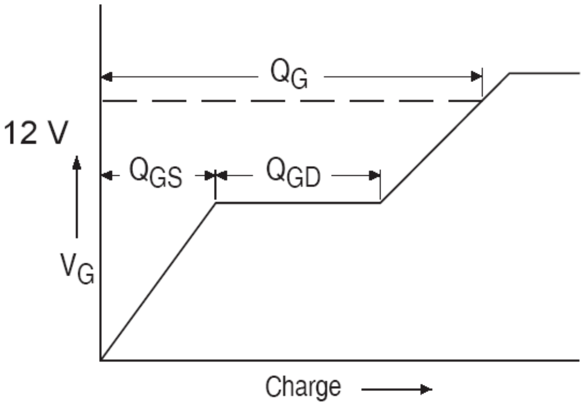


Fig 29a. Gate Charge Waveform

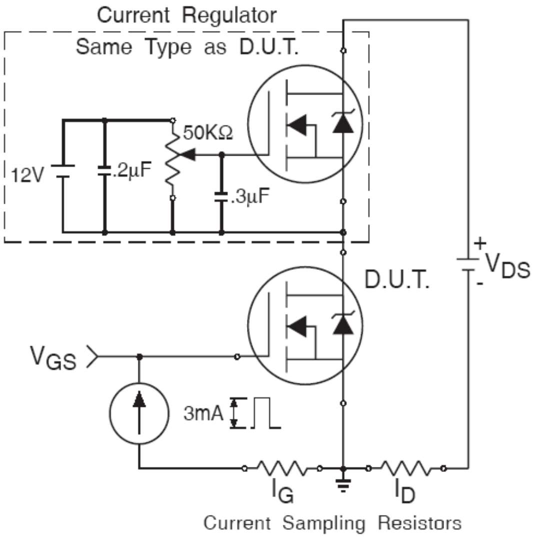


Fig 29b. Gate Charge Test Circuit

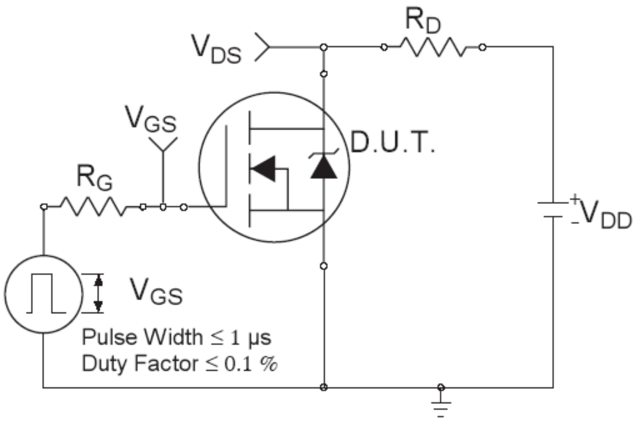


Fig 30a. Switching Time Test Circuit

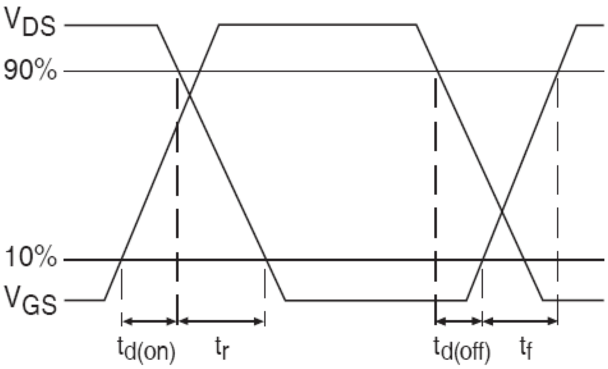
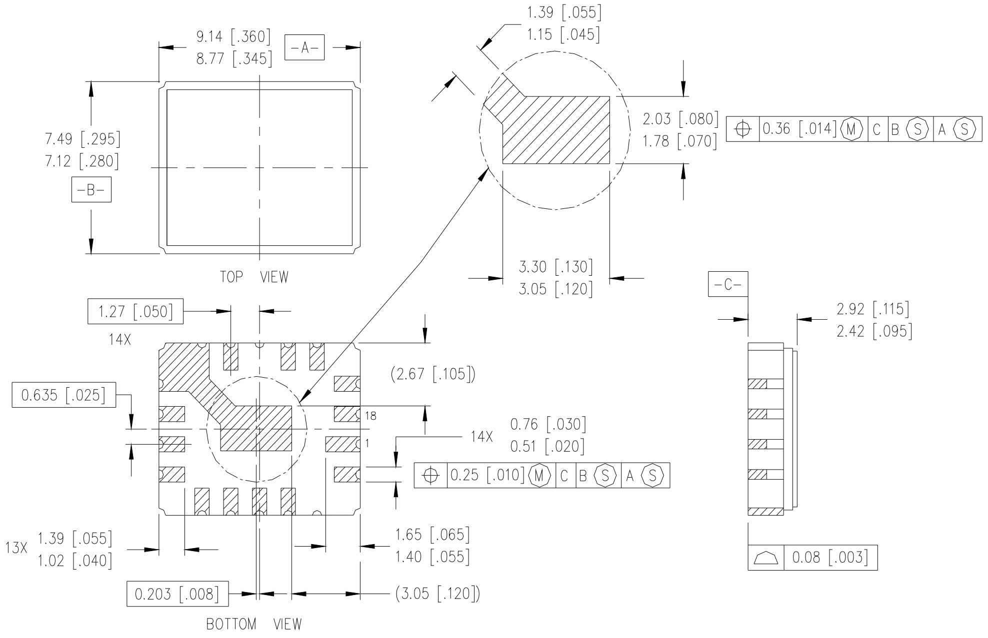
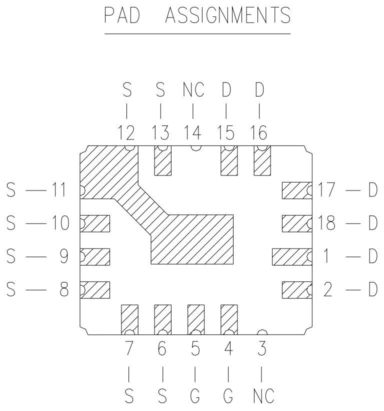


Fig 30b. Switching Time Waveforms

Case Outline and Dimensions — LCC-18





LEGEND

G = GATE
S = SOURCE
D = DRAIN
NC = NO CONNECTION

NOTES:

1. DIMENSIONING & TOLERANCING PER ANSI Y14.5M-1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].

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