

SCHOTTKY RECTIFIER
HIGH EFFICIENCY SERIES

60CIQ045
45Amp*, 45V

Major Ratings and Characteristics

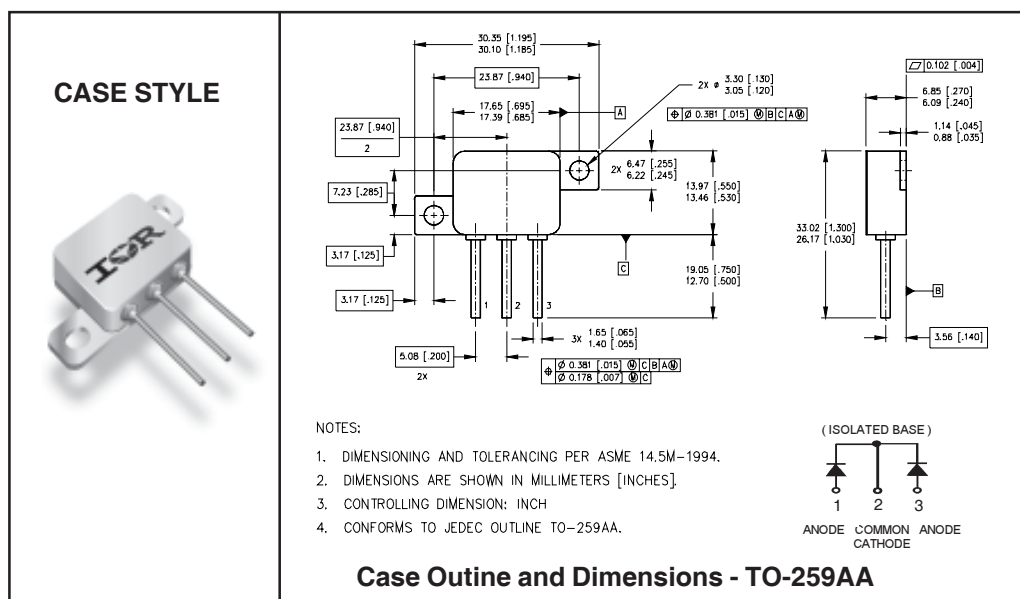
Characteristics	60CIQ045	Units
$I_{F(AV)}$ Rectangular waveform	45*	A
V_{RRM} (Per Leg)	45	V
I_{FSM} @ $t_p = 8.3ms$ half-sine (Per Leg)	400	A
V_F @ 25Apk, $T_J = 125^\circ C$ (Per Leg)	0.62	V
T_J, T_{stg} Operating and storage	-55 to 150	$^\circ C$

* $I_{F(AV)}$ - Current is limited by package

Description/Features

The 60CIQ045 center tap Schottky rectifier has been expressly designed to meet the rigorous requirements of HiRel environments. It is packaged in the hermetic isolated TO-259AA package. The device's forward voltage drop and reverse leakage current are optimized for the lowest power loss and the highest circuit efficiency for typical high frequency switching power supplies and resonant power converters. Full MIL-PRF-19500 quality conformance testing is available on source controlled drawings to TX, TXV and S levels.

- Hermetically Sealed
- Center Tap
- Low Forward Voltage Drop
- High Frequency Operation
- Guard Ring for Enhanced Ruggedness and Long Term Reliability
- 2 Interdigitated Mounting Tabs for Secured, Intimate Heatsink Contact
- Electrically Isolated
- ESD Rating: Class NS per MIL-STD-750, Method 1020



Voltage Ratings

Part number	60CIQ045
V _R Max. DC Reverse Voltage (V)	45
V _{RWM} Max. Working Peak Reverse Voltage (V)	

Absolute Maximum Ratings

Parameters	Limits	Units	Conditions
I _{F(AV)} Max. Average Forward Current See Fig. 5	45*	A	50% duty cycle @ T _C = 100°C, rectangular waveform
I _{FSM} Max. Peak One Cycle Non- Repetitive Surge Current (Per Leg)	400	A	@ t _p = 8.3 ms half-sine

* I_{F(AV)} - Current is limited by package

Electrical Specifications

Parameters	Limits	Units	Conditions	
V _{FM} Max. Forward Voltage Drop (Per Leg) See Fig. 1 ①	0.78	V	@ 25A	
	0.86	V	@ 45A	
	0.71	V	@ 25A	T _J = 25°C
		0.92	V	
	0.62	V	@ 25A	T _J = 125°C
		0.85	V	
I _{RM} Max. Reverse Leakage Current (Per Leg) See Fig. 2 ①	0.8	mA	T _J = 25°C	
	45	mA	T _J = 125°C	
C _T Max. Junction Capacitance (Per Leg)	2600	pF	V _R = 5V _{DC} (1MHz, 25°C)	
L _S Typical Series Inductance (Per Leg)	8.7	nH	Measured from anode lead to cathode lead 6mm (0.025 in.) from package	

Thermal-Mechanical Specifications

Parameters	Limits	Units	Conditions
T _J Max. Junction Temperature Range	-55 to 150	°C	
T _{stg} Max. Storage Temperature Range	-55 to 150	°C	
R _{thJC} Max. Thermal Resistance, Junction to Case (Per Leg)	0.83	°C/W	DC operation See Fig. 4
R _{thJC} Max. Thermal Resistance, Junction to Case (Per Package)	0.42	°C/W	DC operation
wt Weight (Typical)	10.5	g	
Die Size	200X200	mils	
Case Style	TO-259AA		

① Pulse Width < 300µs, Duty Cycle < 2%

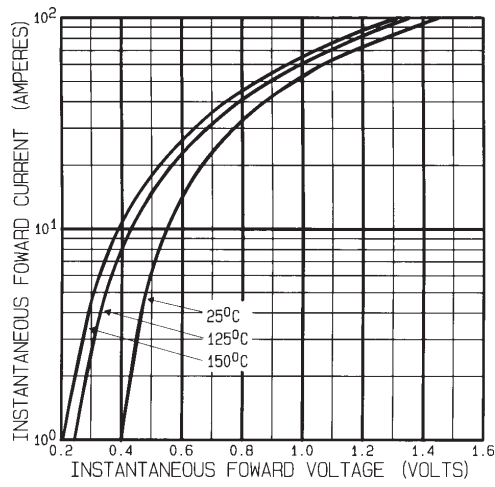


Fig. 1 - Max. Forward Voltage Drop Characteristics (Per Leg)

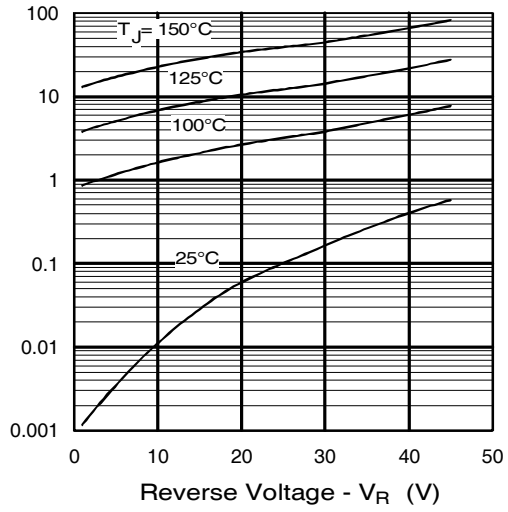


Fig. 2 - Typical Values of Reverse Current Vs. Reverse Voltage (Per Leg)

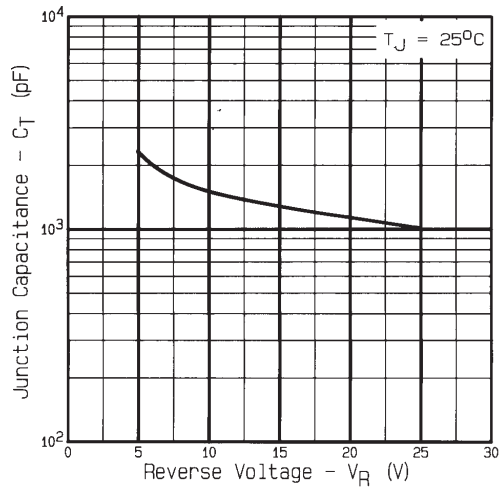


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

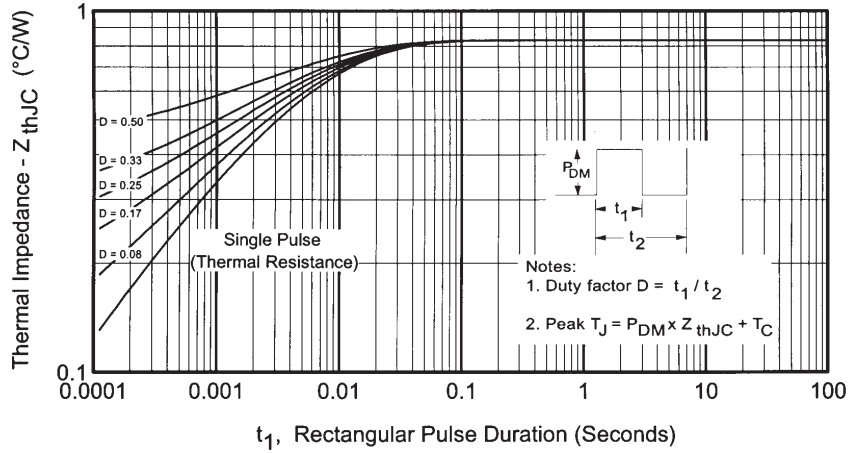


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics (Per Leg)

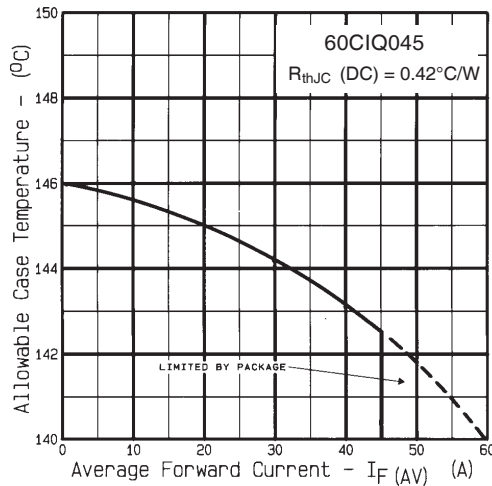


Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current