



## Features

- High efficiency and excellent thermal performance
- Output remote sense
- Input under-voltage, output over-voltage, over-current, short-circuit and over-temperature protections
- Active current share
- Basic insulation, 2,250Vdc input to output isolation
- Qualification/Screening satisfy IPC-9592
- Wide operating temperature range from -40°C to +100°C



## Part Numbering System

FYV	EC	□□□	□	□□□	□	□	□	(□)	-	□
Series Name	Input Voltage	Output Voltage	Enabling Logic	Rated Output Current	Pin Length	Options 1	Options 2	Suffix	-	Operating Temperature Grade (°C)*
	EC: 300-500V	Example: 120: 12V	P: Positive N: Negative	Example: 080: 80A	N: 0.130" R: 0.165"	0: Latch off 2: Auto-restart	P: Standard	Variation code		C: -20 to +100 H: -40 to +100

\* Operating temperature is the temperature measured at the center of the baseplate.

## Available Codes:

Output Voltage	8.5V	12V	28V	48V
Output Current	78A	80A	28A, 36A	21A

## Absolute Maximum Rating

Excessive stresses over these absolute maximum ratings can cause permanent damage to the converter. Operation should be limited to the conditions outlined under the Electrical Specification section.

Parameter	Min	Max	Unit
Input Voltage (continuous operating)	-0.5	500	V
Input Voltage (continuous, non-operating)	-	550	V
Input Voltage (<100ms, operating)	-	550	V
Storage Temperature	-55	125	°C

## Electrical Specifications

These specifications are valid over the converter's full range of input voltage, resistive load, and temperature unless noted otherwise.

### Input Specifications

Parameter	Min	Typical	Max	Unit
Input Voltage	300	400	500	V
Input Turn-on Voltage Threshold	280	290	300	V
Input Turn-off Voltage Threshold	260	270	280	V

For 8.5V/78A & 28V/28A output modules, the (Min, Typical, Max) value of "Input Turn-on Voltage Threshold" is (270, 278, 290)V, the (Min, Typical, Max) value of "Input Turn-off Voltage Threshold" is (240, 245, 260)V.

### Output Specifications

Parameter	Min	Typical	Max	Unit
Output Voltage Set Point Accuracy (typical Vin, full load, Ta = 25°C)	-1.5	-	+1.5	%Vo
Output Voltage Set Point Accuracy (over all conditions)	-3.0	-	+3.0	%Vo
Output Regulation:				
Line Regulation (full range input voltage, 1/2 full load)	-	0.2	0.5	%Vo
Load Regulation (full range load, typical Vin)	-	0.2	0.5	%Vo
Temperature (Ta = -40°C to 85 °C)	-	0.1	-	%Vo
Output Trim Range in % of typical Vo	80	-	110	%

### General Specifications

Parameter	Min	Typical	Max	Unit
Remote Enable				
Logic Low:				
ION/OFF = 1.0mA	0	-	1.2	V
VON/OFF = 0.0V	-	-	1.0	mA
Logic High:				
ION/OFF = 0.0µA	3.5	-	15	V
Leakage Current	-	-	50	µA
Isolation Capacitance <sup>1</sup>	-	1,200	-	pF
Insulation Resistance	100	-	-	MΩ

<sup>1</sup> For 8.5V/78A & 28V/28A output modules, the typical value of "Isolation Capacitance" is 4,700pF and the min. value of "Insulation Resistance" is 10MΩ.

## Module Specific Specifications

### 8.5V/78A Module (FYVEC085x078xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	3.5	A
Quiescent Input Current (typical Vin)	-	25	35	mA
Standby Input Current	-	5	10	mA
Efficiency (typical Vin, full load, Ta = 25°C)	-	91.0	-	%
Output Voltage Set Point	-	8.5	-	V
Output Power	0	-	663	W
Output Over Current Protection Set Point	86	90	94	A
Output Over Voltage Protection Set Point	9.5	10.0	10.5	V
Output Ripple Frequency	212	250	288	kHz
Output Ripple and Noise Voltage RMS	-	20	45	mVrms
Peak-to-peak (5Hz to 20MHz bandwidth, typical Vin)	-	100	225	mVp-p
External Load Capacitance	-	-	10,000	µF

### 12V/80A Module (FYVEC120x080xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	4.7	A
Quiescent Input Current (typical Vin)	-	35	45	mA
Standby Input Current	-	5	8	mA
Efficiency (typical Vin, full load, Ta = 25°C)	-	93.2	-	%
Output Voltage Set Point	-	12.0	-	V
Output Power	0	-	960	W
Output Over Current Protection Set Point	88.0	93.5	97.0	A
Output Over Voltage Protection Set Point	13.8	15.0	16.0	V
Output Ripple Frequency	204	240	276	kHz
Output Ripple and Noise Voltage RMS	-	45	75	mVrms
Peak-to-peak (5Hz to 20MHz bandwidth, typical Vin)	-	150	250	mVp-p
External Load Capacitance	-	-	10,000	µF

### 28V/28A Module (FYVEC280x028xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	3.5	A
Quiescent Input Current (typical Vin)	-	30	40	mA
Standby Input Current	-	5	10	mA
Efficiency (typical Vin, full load, Ta = 25°C)	-	93.0	-	%
Output Voltage Set Point	-	28.0	-	V
Output Power	0	-	800	W
Output Over Current Protection Set Point	32.5	34.0	35.0	A
Output Over Voltage Protection Set Point	33	35	36	V
Output Ripple Frequency	212	250	288	kHz
Output Ripple and Noise Voltage RMS	-	30	50	mVrms
Peak-to-peak (5Hz to 20MHz bandwidth, typical Vin)	-	150	250	mVp-p
External Load Capacitance	-	-	10,000	µF

**28V/36A Module (FYVEC280x036xxx(x)-x)**

Parameter	Min	Typical	Max	Unit
Input Current	-	-	4.7	A
Quiescent Input Current (typical Vin)	-	28	35	mA
Standby Input Current	-	5	8	mA
Efficiency (typical Vin, full load, Ta = 25°C)	-	94.2	-	%
Output Voltage Set Point	-	28.0	-	V
Output Power	0	-	1,008	W
Output Over Current Protection Set Point	39	45	51	A
Output Over Voltage Protection Set Point	32.2	35.4	38.4	V
Output Ripple Frequency	200	240	280	kHz
Output Ripple and Noise Voltage RMS	-	50	100	mVrms
Peak-to-peak (5Hz to 20MHz bandwidth, typical Vin)	-	120	200	mVp-p
External Load Capacitance	-	-	10,000	μF

**48V/21A Module (FYVEC480x021xxx(x)-x)**

Parameter	Min	Typical	Max	Unit
Input Current	-	-	4.8	A
Quiescent Input Current (typical Vin)	-	31	40	mA
Standby Input Current	-	5	7	mA
Efficiency (typical Vin, full load, Ta = 25°C)	-	94.5	-	%
Output Voltage Set Point	-	48.0	-	V
Output Power	0	-	1,008	W
Output Over Current Protection Set Point	24	26	28	A
Output Over Voltage Protection Set Point	55	60	65	V
Output Ripple Frequency	210	250	280	kHz
Output Ripple and Noise Voltage RMS	-	100	150	mVrms
Peak-to-peak (5Hz to 20MHz bandwidth, typical Vin)	-	300	450	mVp-p
External Load Capacitance	-	-	10,000	μF

## Feature Descriptions

### Remote ON/OFF

The converter can be turned on and off by changing the voltage between the ON/OFF pin and Vin(-). The FYVEC Series of converters is available with factory selectable positive logic and negative logic.

For the negative control logic, the converter is ON when the ON/OFF pin is at a logic low level and OFF when the ON/OFF pin is at a logic high level. For the positive control logic, the converter is ON when the ON/OFF pin is at a logic high level and OFF when the ON/OFF pin is at a logic low level.

With the internal pull-up circuitry, a simple external switch between the ON/OFF pin and Vin(-) can control the converter. A few example circuits for controlling the ON/OFF pin are shown in Figures 1, 2 and 3.

The logic low level is from 0V to 1.2V and the maximum sink current during logic low is 1mA. The external switch must be capable of maintaining a logic-low level while sinking up to this current. The logic high level is from 3.5V to 15V. The converter has an internal pull-up circuit that ensures the ON/OFF pin at a high logic level when the leakage current at ON/OFF pin is no greater than 50µA.

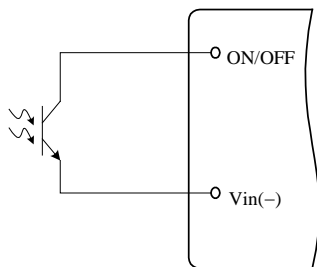


Figure 1. Opto Coupler Enable Circuit

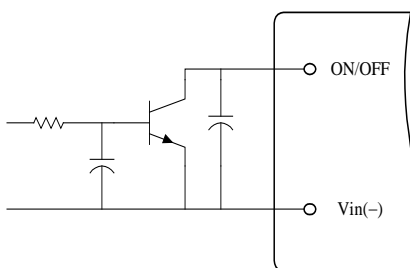


Figure 2. Open Collector Enable Circuit

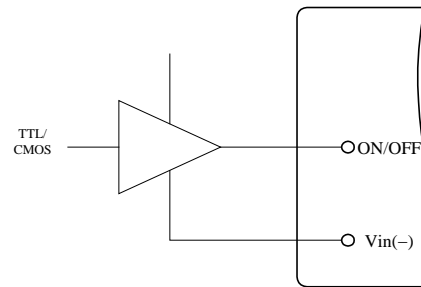


Figure 3. Direct Logic Drive

### Remote SENSE

The remote SENSE pins are used to sense the voltage at the load point to accurately regulate the load voltage and eliminate the impact of the voltage drop in the power distribution path.

SENSE (+) and SENSE (-) pins should be connected between the points where voltage regulation is desired. The voltage between the SENSE pins and the output pins must not exceed the smaller of 0.5V or 10% of typical output voltage.

$$[V_{out (+)} - V_{out (-)}] - [SENSE (+) - SENSE (-)] < \text{MIN} \{0.5V, 10\%V_o\}$$

When remote sense is not used, the SENSE pins should be connected to their corresponding output pins. If the SENSE pins are left floating, the converter will deliver an output voltage slightly higher than its specified typical output voltage.

### Output Voltage Adjustment (Trim)

The trim pin allows the user to adjust the output voltage set point. To increase the output voltage, an external resistor is connected between the TRIM pin and SENSE(+). To decrease the output voltage, an external resistor is connected between the TRIM pin and SENSE(-). The output voltage trim range is 80% to 110% of the specified typical output voltage.

The circuit configuration for trim down operation is shown in Figure 4. To decrease the output voltage, the value of the external resistor should be

$$R_{down} = \left( \frac{202.92}{\Delta} - 7.14 \right) \text{ (k}\Omega\text{)}$$

Where

$$\Delta = \left( \frac{|V_{nom} - V_{adj}|}{V_{nom}} \right) \times 100$$

And

$V_{nom}$  = Typical Output Voltage  
 $V_{adj}$  = Adjusted Output Voltage

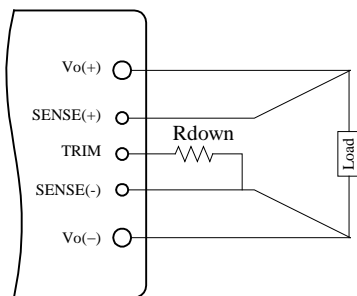
The circuit configuration for trim up operation is shown in Figure 5. To increase the output voltage, the value of the resistor should be

$$R_{up} = \left( \frac{2.05V_o(100 + \Delta)}{1.225\Delta} - \frac{202.92}{\Delta} - 7.14 \right) \text{ (k}\Omega\text{)}$$

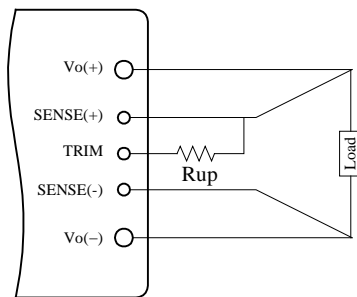
Where

$V_o$  = Typical Output Voltage

As the output voltage at the converter output terminals are higher than the specified typical level when using the trim up and/or remote sense functions, it is important to make sure that the voltage at the output terminals does not exceed the maximum power rating of the converter as given in the specifications table.



**Figure 4.** Circuit to Decrease Output Voltage

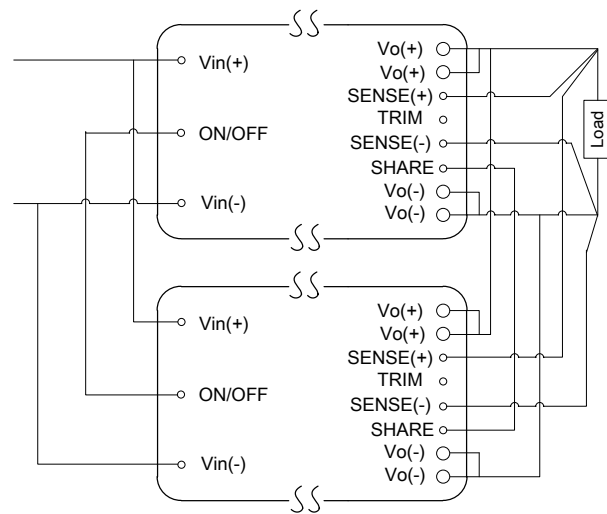


**Figure 5.** Circuit to Increase Output Voltage

### Active Current Sharing (Parallel)

Share pin is designed for active current share among modules in parallel.

The active current share feature allows multiple converters to share load current. For the parallel operation of multiple converters, all Share pins should be connected together. It is suggested to have a copper plane on the system board for  $V_o(-)$  to reduce the ground noise impact on the current share accuracy. The loop formed by the trace connecting the SHARE pins and the  $V_o(-)$  shall be minimized to avoid noise coupling into the current sharing circuitry. To have synchronized start-up of all modules in parallel operation, the parallel feature should be accompanied by the latch-off protection feature if the output of the modules in parallel will be directly connected together. The FYVEC converter has an internal oring FET at its output. The output pins of the converters in parallel can be directly connected together. A typical current share schemes for the FYVEC series of converters is shown in Figure 6.



**Figure 6.** Circuit Configuration for Active Current Sharing

If parallel feature is not needed, just let share pin open.

Up to eight FYVEC converters can operate in parallel to share the load current. When the beat frequency of converters in parallel is a concern, synchronization among the converters is often the choice.

### Input Under-Voltage Lockout

This feature prevents the converter from starting until the input voltage reaches the turn-on voltage threshold, and keeps the converter running until the input voltage falls below the turn-off voltage threshold. Both turn-on and turn-off voltage thresholds are defined in the Input Specifications table.

### Output Over-Current Protection (OCP)

This converter can be ordered in either latch-off or auto-restart version upon OCP, OVP, and OTP.

With the latch-off version, the converter will latch off when the load current exceeds the limit. The converter can be restarted by toggling the ON/OFF switch or recycling the input voltage.

With the auto-restart version, the converter will operate in a hiccup mode (repeatedly try to restart) until the cause of the over-current condition is cleared.

### Output Over-Voltage Protection (OVP)

With the latch-off version, the converter will latch off when the output voltage exceeds the limit. The converter can be restarted by toggling the ON/OFF switch or recycling the input voltage.

With the auto-restart version, the converter will operate in a hiccup mode (repeatedly try to restart) until the cause of the over-voltage condition is cleared.

### Over Temperature Protection (OTP)

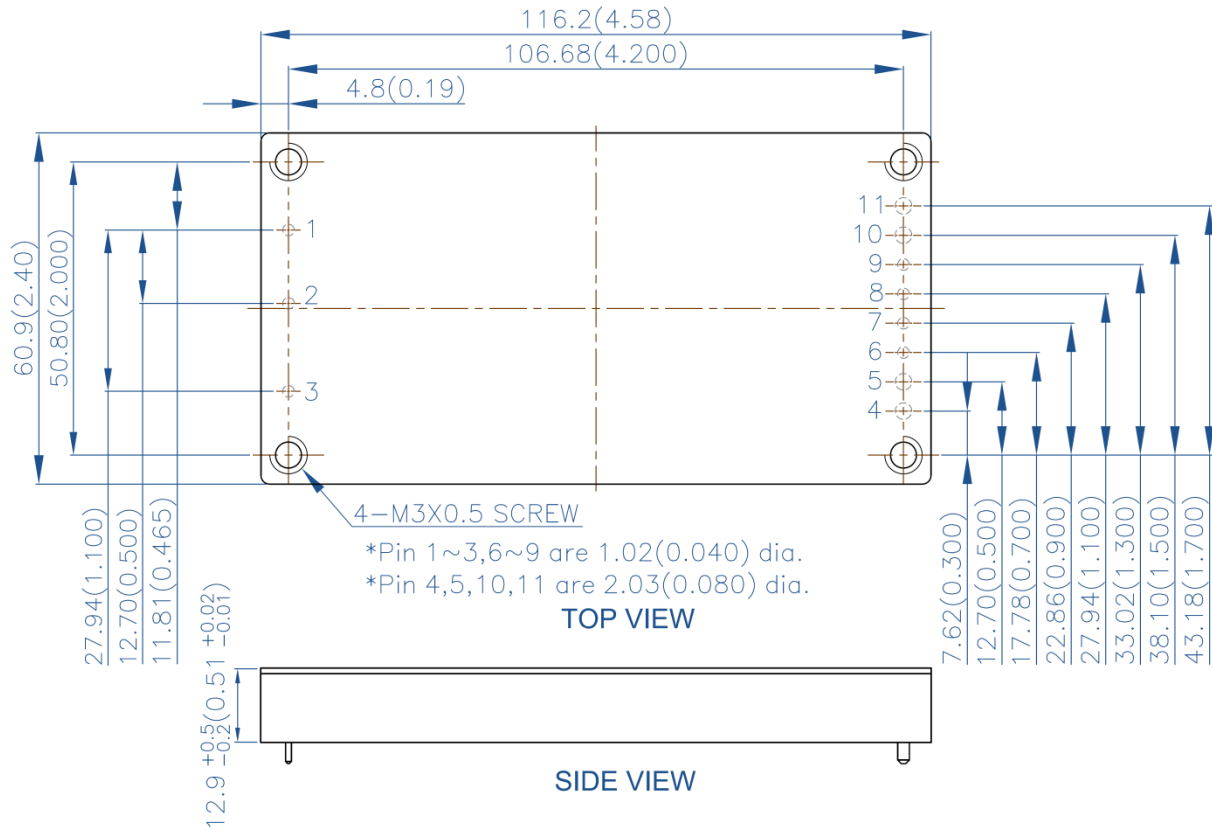
With the latch-off version, the converter will shut down and latch off if an over-temperature condition is detected. The converter has a temperature sensor located at a carefully selected position, which represents the thermal condition of key components of the converter. The thermal shutdown circuit is designed to turn the converter off when the temperature at the sensor reaches 120°C. The module can be restarted by toggling the ON/OFF switch or recycling the input voltage.

With the auto-restart version, the converter will resume operation after the converter cools down.

### Design Considerations

As with any DC-DC converter, the stability of the FYVEC converter may be compromised if the source impedance is too high or inductive. It's desirable to keep the input source ac-impedance as low as possible. Although the converters are designed to be stable without adding external input capacitors for typical source impedance, it is recommended to add 10 $\mu$ F low ESR electrolytic capacitors at the input of the converter for each 100W output power, which reduces the potential negative impact of the source impedance on the converter stability. These electrolytic capacitors should have sufficient RMS current rating over the operating temperature range.

The converter is designed to be stable without additional output capacitors. To further reduce the output voltage ripple or improve the transient response, additional output capacitors are often used in applications. When additional output capacitors are used, a combination of ceramic capacitors and tantalum/polymer capacitors shall be used to provide good filtering while assuring the stability of the converter.

**Mechanical Drawing**


Pin	Name	Function
1	Vin(+)	Positive input voltage
2	ON/OFF(-)	Remote control
3	Vin(-)	Negative input voltage
4	Vout(-)	Negative output voltage
5	Vout(-)	Negative output voltage
6	SHARE	Current share
7	SENSE(-)	Negative remote sense
8	Trim	Output voltage adjustment
9	SENSE(+)	Positive remote sense
10	Vout(+)	Positive output voltage
11	Vout(+)	Positive output voltage

**Notes:**

- All dimensions in mm (inches)  
 Tolerances: .x ± .5 (.xx ± 0.02)  
 .xx ± .25 (.xxx ± 0.010)
- Input and function pins are 1.02mm (0.040") dia. with +/- 0.10mm (0.004") tolerance. The recommended diameter of the receiving hole is 1.42mm (0.056").
- Output pins are 2.03mm (0.080") dia. with +/- 0.10mm (0.004") tolerance. The recommended diameter of the receiving hole is 2.44mm (0.096").
- All pins are Copper Alloy, Matte Tin finish with Nickel under plating.
- Typical weight: 300g.
- Workmanship meets or exceeds IPC-A-610 Class II.
- Torque applied on screw should not exceed 6in-lb. (0.7 Nm).
- Baseplate flatness tolerance is 0.10mm (0.004") TIR for surface.