



Features

- High efficiency and excellent thermal performance
- Output remote sense
- Input under-voltage, output over-voltage, over-current, short-circuit and over-temperature protections
- Active current share
- Basic insulation, 3,000Vac input to output isolation
- Compliance with EN50155
- Wide operating temperature range from -40°C to +100°C



Part Numbering System

FYR	7A	□□□	□	□□□	□	□	□	(□)	-	□
Series Name	Input Voltage	Output Voltage	Enabling Logic	Rated Output Current	Pin Length	Options 1	Options 2	Suffix	-	Operating Temperature Grade (°C)*
	7A: 66-160V	Example: 120: 12V	P: Positive N: Negative	Example: 025: 25A	J: 0.220"	0: Latch off 2: Auto-restart	S: Metal shield, flange	Variation code		C: -20 to +100 H: -40 to +100

* Operating temperature is the temperature measured at the center of the baseplate.

Available Codes:

Output Voltage	12V	24V	48V
Output Current	25A, 33A	16.7A	6.3A, 8.3A

Absolute Maximum Rating

Excessive stresses over these absolute maximum ratings can cause permanent damage to the converter. Operation should be limited to the conditions outlined under the Electrical Specification section.

Parameter	Min	Max	Unit
Input Voltage (continuous operating)	-0.5	160	V
Input Voltage (continuous, non-operating)	-	250	V
Input Voltage (<100ms, operating)	-	250	V
Storage Temperature	-55	125	°C

Electrical Specifications

These specifications are valid over the converter's full range of input voltage, resistive load, and temperature unless noted otherwise.

Input Specifications

Parameter	Min	Typical	Max	Unit
Input Voltage	66	110	160	V
Input Turn-on Voltage Threshold	62.5	64.0	65.5	V
Input Turn-off Voltage Threshold	58.0	60.0	62.0	V

Output Specifications

Parameter	Min	Typical	Max	Unit
Output Voltage Set Point Accuracy (typical Vin, full load, Ta = 25°C)	-1.5	-	+1.5	%Vo
Output Voltage Set Point Accuracy (over all conditions)	-3.0	-	+3.0	%Vo
Output Regulation: Line Regulation (full range input voltage, 1/2 full load)	-	0.2	0.5	%Vo
Load Regulation (full range load, typical Vin)	-	0.2	0.5	
Temperature (Ta = -40°C to 85 °C)	-	0.1	-	
Output Trim Range in % of typical Vo	80	-	110	%

General Specifications

Parameter	Min	Typical	Max	Unit	
Remote Enable					
Logic Low:					
ION/OFF = 1.0mA	0	-	1.2	V	
VON/OFF = 0.0V	-	-	1.0	mA	
Logic High:					
ION/OFF = 0.0µA	3.5	-	15	V	
Leakage Current	-	-	50	µA	
Isolation Voltage	Input-Output	-	-	3,000	Vac
	Input-Baseplate	-	-	1,500	Vdc
	Output-Baseplate	-	-	500	Vdc
Isolation Capacitance	-	2,350	-	pF	
Insulation Resistance	10	-	-	MΩ	

Module Specific Specifications

12V/25A Module (FYR7A120x025xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	8	A
Quiescent Input Current (typical Vin)	-	80	100	mA
Standby Input Current	-	4	6	mA
Efficiency (typical Vin, full load, Ta = 25°C)	-	90.0	-	%
Output Voltage Set Point	-	12.0	-	V
Output Power	0	-	300	W
Output Over Current Protection Set Point	103	-	140	%
Output Over Voltage Protection Set Point	115	125	135	%
Output Ripple Frequency	200	220	240	kHz
Output Ripple and Noise Voltage RMS	-	12	20	mVrms
Peak-to-peak (5Hz to 20MHz bandwidth, typical Vin)	-	50	80	mVp-p
External Load Capacitance	-	-	3,000	µF

12V/33A Module (FYR7A120x033xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	10	A
Quiescent Input Current (typical Vin)	-	80	110	mA
Standby Input Current	-	5	6	mA
Efficiency (typical Vin, full load, Ta = 25°C)	-	90.0	-	%
Output Voltage Set Point	-	12.0	-	V
Output Power	0	-	400	W
Output Over Current Protection Set Point	103	-	140	%
Output Over Voltage Protection Set Point	115	125	135	%
Output Ripple Frequency	180	200	220	kHz
Output Ripple and Noise Voltage RMS	-	10	15	mVrms
Peak-to-peak (5Hz to 20MHz bandwidth, typical Vin)	-	40	60	mVp-p
External Load Capacitance	-	-	3,000	µF

24V/16.7A Module (FYR7A240x167xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	10	A
Quiescent Input Current (typical Vin)	-	85	120	mA
Standby Input Current	-	4	6	mA
Efficiency (typical Vin, full load, Ta = 25°C)	-	91.5	-	%
Output Voltage Set Point	-	24.0	-	V
Output Power	0	-	400.8	W
Output Over Current Protection Set Point	103	-	140	%
Output Over Voltage Protection Set Point	115	125	135	%
Output Ripple Frequency	200	220	240	kHz
Output Ripple and Noise Voltage RMS	-	10	20	mVrms
Peak-to-peak (5Hz to 20MHz bandwidth, typical Vin)	-	50	80	mVp-p
External Load Capacitance	-	-	2,000	µF

48V/6.3A Module (FYR7A480x6A3xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	6.5	A
Quiescent Input Current (typical Vin)	-	90	120	mA
Standby Input Current	-	4	6	mA
Efficiency (typical Vin, full load, Ta = 25°C)	-	92.0	-	%
Output Voltage Set Point	-	48.0	-	V
Output Power	0	-	302.4	W
Output Over Current Protection Set Point	103	-	140	%
Output Over Voltage Protection Set Point	115	125	135	%
Output Ripple Frequency	200	220	240	kHz
Output Ripple and Noise Voltage RMS	-	30	45	mVrms
Peak-to-peak (5Hz to 20MHz bandwidth, typical Vin)	-	120	160	mVp-p
External Load Capacitance	-	-	1,000	μF

48V/8.3A Module (FYR7A480x8A3xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	10	A
Quiescent Input Current (typical Vin)	-	90	100	mA
Standby Input Current	-	4	6	mA
Efficiency (typical Vin, full load, Ta = 25°C)	-	92.0	-	%
Output Voltage Set Point	-	48.0	-	V
Output Power	0	-	400	W
Output Over Current Protection Set Point	103	-	140	%
Output Over Voltage Protection Set Point	115	125	135	%
Output Ripple Frequency	200	220	240	kHz
Output Ripple and Noise Voltage RMS	-	40	60	mVrms
Peak-to-peak (5Hz to 20MHz bandwidth, typical Vin)	-	150	220	mVp-p
External Load Capacitance	-	-	200	μF

Feature Descriptions

Remote ON/OFF

The converter can be turned on and off by changing the voltage between the ON/OFF pin and Vin(-). The FYR7A Series of converters is available with factory selectable positive logic and negative logic.

For the negative control logic, the converter is ON when the ON/OFF pin is at a logic low level and OFF when the ON/OFF pin is at a logic high level. For the positive control logic, the converter is ON when the ON/OFF pin is at a logic high level and OFF when the ON/OFF pin is at a logic low level.

With the internal pull-up circuitry, a simple external switch between the ON/OFF pin and Vin(-) can control the converter. A few example circuits for controlling the ON/OFF pin are shown in Figures 1, 2 and 3.

The logic low level is from 0V to 1.2V and the maximum sink current during logic low is 1mA. The external switch must be capable of maintaining a logic-low level while sinking up to this current. The logic high level is from 3.5V to 15V. The converter has an internal pull-up circuit that ensures the ON/OFF pin at a high logic level when the leakage current at ON/OFF pin is no greater than 50µA.

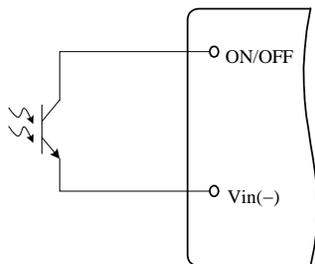


Figure 1. Opto Coupler Enable Circuit

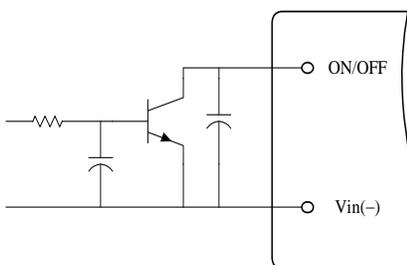


Figure 2. Open Collector Enable Circuit

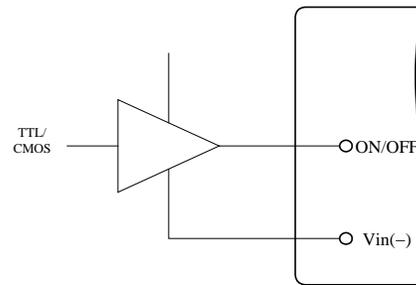


Figure 3. Direct Logic Drive

Remote SENSE

The remote SENSE pins are used to sense the voltage at the load point to accurately regulate the load voltage and eliminate the impact of the voltage drop in the power distribution path.

SENSE (+) and SENSE (-) pins should be connected between the points where voltage regulation is desired. The voltage between the SENSE pins and the output pins must not exceed the smaller of 0.5V or 10% of typical output voltage.

$$[V_{out (+)} - V_{out (-)}] - [SENSE (+) - SENSE (-)] < \text{MIN} \{0.5V, 10\%V_o\}$$

When remote sense is not used, the SENSE pins should be connected to their corresponding output pins. If the SENSE pins are left floating, the converter will deliver an output voltage slightly higher than its specified typical output voltage.

Output Voltage Adjustment (Trim)

The trim pin allows the user to adjust the output voltage set point. To increase the output voltage, an external resistor is connected between the TRIM pin and SENSE(+). To decrease the output voltage, an external resistor is connected between the TRIM pin and SENSE(-). The output voltage trim range is 80% to 110% of the specified typical output voltage.

The circuit configuration for trim down operation is shown in Figure 4. To decrease the output voltage, the value of the external resistor should be

$$R_{down} = \left(\frac{511}{\Delta} - 10.22 \right) (k\Omega)$$

Where

$$\Delta = \left(\frac{|V_{nom} - V_{adj}|}{V_{nom}} \right) \times 100$$

And

V_{nom} = Typical Output Voltage
 V_{adj} = Adjusted Output Voltage

The circuit configuration for trim up operation is shown in Figure 5. To increase the output voltage, the value of the resistor should be

$$R_{up} = \left(\frac{5.11V_o(100 + \Delta)}{1.225\Delta} - \frac{511}{\Delta} - 10.22 \right) (k\Omega)$$

Where

V_o = Typical Output Voltage

As the output voltage at the converter output terminals are higher than the specified typical level when using the trim up and/or remote sense functions, it is important to make sure that the voltage at the output terminals does not exceed the maximum power rating of the converter as given in the specifications table.

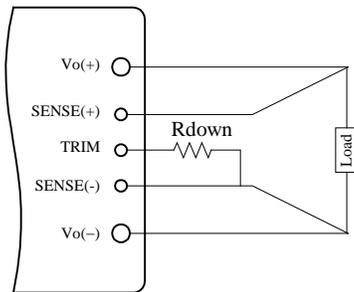


Figure 4. Circuit to Decrease Output Voltage

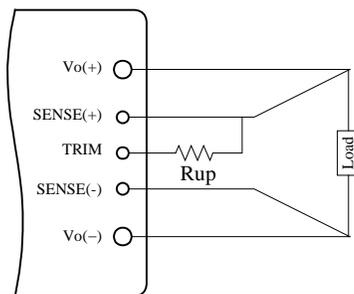


Figure 5. Circuit to Increase Output Voltage

Active Current Sharing (Parallel)

Share pin is designed for active current share among modules in parallel.

The active current share feature allows multiple converters to share load current. For the parallel operation of multiple converters, the Share pins of the converter should be connected together. It is suggested to have a ground plane on the system board for $V_{in}(-)$ to reduce the ground noise impact on the current sharing accuracy. The loop formed by the trace connecting the Share pins and the ground trace should be minimized to avoid noise coupling into the current share circuitry. The FYV7A converter with current share feature has an internal oring FET at its output. The output pins of the current sharing converters can be directly connected together. A typical current share schemes for the FYV7A series of converters is shown in Figure 6.

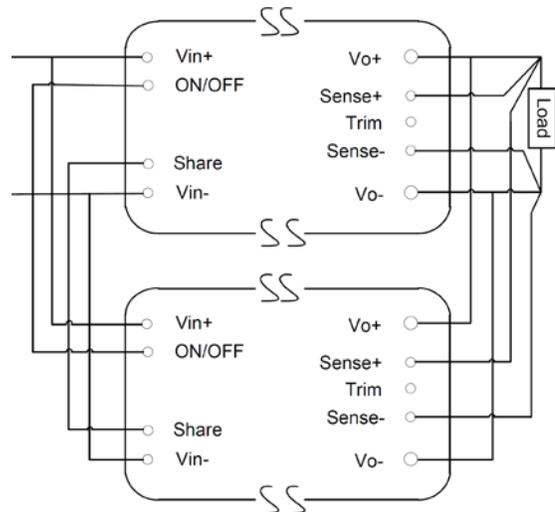


Figure 6. Circuit Configuration for Active Current Sharing

If parallel feature is not needed, just let share pin open.

Input Under-Voltage Lockout

This feature prevents the converter from starting until the input voltage reaches the turn-on voltage threshold, and keeps the converter running until the input voltage falls below the turn-off voltage threshold. Both turn-on and turn-off voltage thresholds are defined in the Input Specifications table.

Output Over-Current Protection (OCP)

This converter can be ordered in either latch-off or auto-restart version upon OCP, OVP, and OTP.

With the latch-off version, the converter will latch off when the load current exceeds the limit. The

converter can be restarted by toggling the ON/OFF switch or recycling the input voltage.

With the auto-restart version, the converter will operate in a hiccup mode (repeatedly try to restart) until the cause of the over-current condition is cleared.

Output Over-Voltage Protection (OVP)

With the latch-off version, the converter will latch off when the output voltage exceeds the limit. The converter can be restarted by toggling the ON/OFF switch or recycling the input voltage.

With the auto-restart version, the converter will operate in a hiccup mode (repeatedly try to restart) until the cause of the over-voltage condition is cleared.

Over Temperature Protection (OTP)

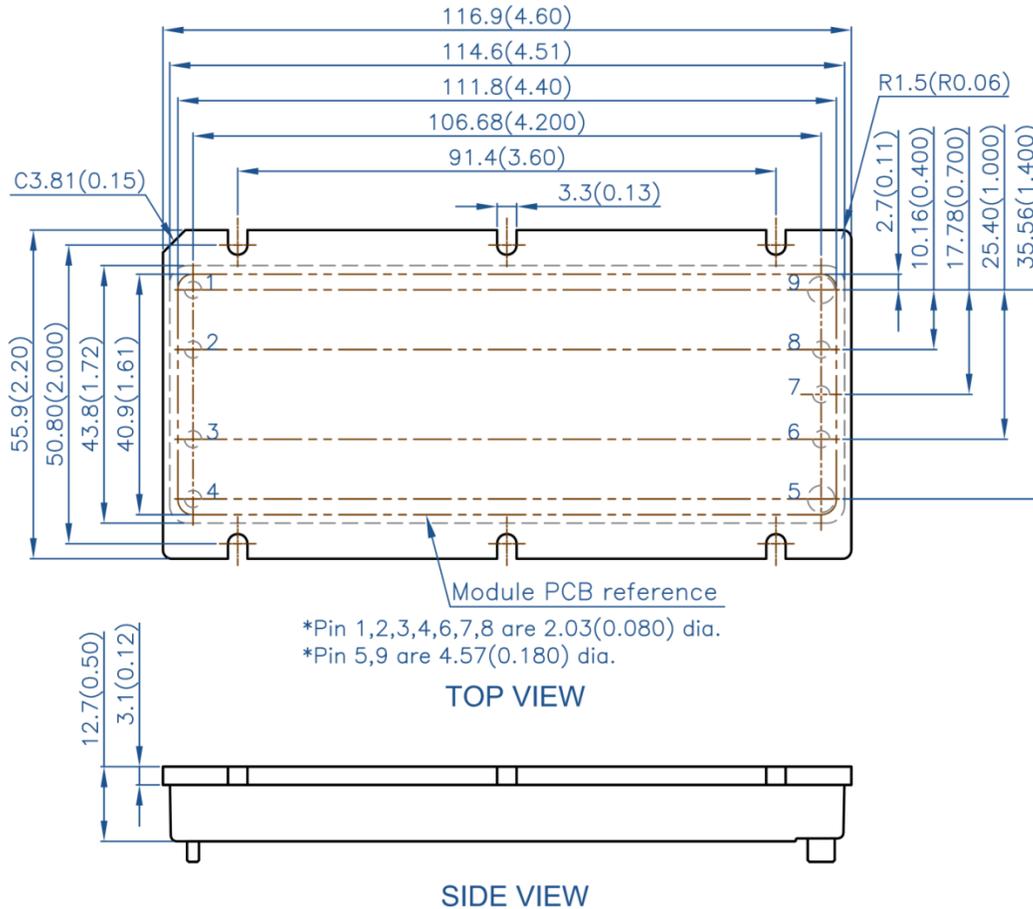
With the latch-off version, the converter will shut down and latch off if an over-temperature condition is detected. The converter has a temperature sensor located at a carefully selected position, which represents the thermal condition of key components of the converter. The thermal shutdown circuit is designed to turn the converter off when the temperature at the sensor reaches 120°C. The module can be restarted by toggling the ON/OFF switch or recycling the input voltage.

With the auto-restart version, the converter will resume operation after the converter cools down.

Design Considerations

As with any DC-DC converter, the stability of the FYR7A converter may be compromised if the source impedance is too high or inductive. It's desirable to keep the input source ac-impedance as low as possible. Although the converters are designed to be stable without adding external input capacitors for typical source impedance, it is recommended to add 100 μ F low ESR electrolytic capacitors at the input of the converter for each 100W output power, which reduces the potential negative impact of the source impedance on the converter stability. These electrolytic capacitors should have sufficient RMS current rating over the operating temperature range.

The converter is designed to be stable without additional output capacitors. To further reduce the output voltage ripple or improve the transient response, additional output capacitors are often used in applications. When additional output capacitors are used, a combination of ceramic capacitors and tantalum/polymer capacitors shall be used to provide good filtering while assuring the stability of the converter.

Mechanical Drawing


Pin	Name	Function
1	Vin(+)	Positive input voltage
2	ON/OFF	Remote control
3	SHARE	Current share
4	Vin(-)	Negative input voltage
5	Vout(-)	Negative output voltage
6	SENSE(-)	Negative remote sense
7	TRIM	Output voltage adjustment
8	SENSE(+)	Positive remote sense
9	Vout(+)	Positive output voltage

Notes:

- All dimensions in mm (inches)
Tolerances: .x ± .5 (.xx ± 0.02)
.xx ± .25 (.xxx ± 0.010)
- Input and function pins are 2.03mm (0.080") dia. with +/- 0.10mm (0.004") tolerance. The recommended diameter of the receiving hole is 2.44mm (0.096").
- Output pins are 4.57mm (0.180") dia. with +/- 0.10mm (0.004") tolerance. The recommended diameter of the receiving hole is 5.08mm (0.200").
- All pins are Copper Alloy, Matte Tin finish with Nickel under plating.
- Typical weight: 210g.
- Workmanship meets or exceeds IPC-A-610 Class II.
- Torque applied on screw should not exceed 6in-lb. (0.7 Nm).
- Baseplate flatness tolerance is 0.10mm (0.004") TIR for surface.